



DWG NO DC2785A

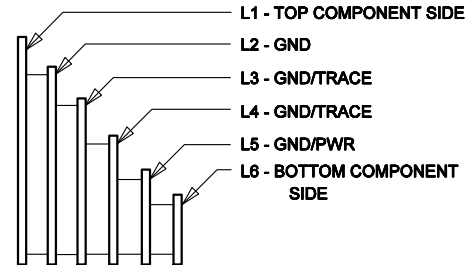
SH 1

REV 3

ADI CONFIDENTIAL - For Customer Use Only

## REVISION HISTORY

ECO	REV	DESCRIPTION	DATE	APPROVED
	2	PROD	03/11/19	<i>ASL</i>

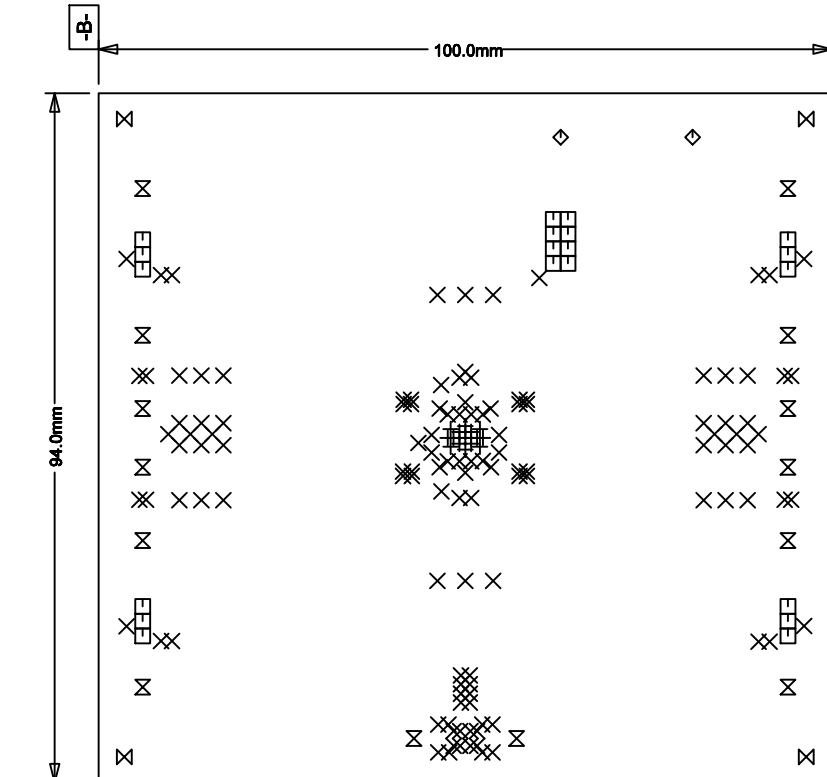


## LAYER CONSTRUCTION

## NOTES: UNLESS OTHERWISE SPECIFIED,

- ARTWORK P/N DC2785A REV 3.
- FAB PER IPC-A-600
- PCB SHALL MEET RoHS COMPLIANCE.
- MATERIAL:
  - LAMINATE: EPOXY FIBERGLASS, NEMA GRADE FR-4  
FLAMMABILITY RATING: 94 V-2 MINIMUM.
  - THICKNESS: 1.6 +/- 0.1 mm
  - CLAD: 2 OZ. (50um) EXTERNAL LAYERS.  
1 OZ. (25um) COPPER ON ALL INTERNAL LAYERS.
- FINISH:
  - COATING: SOLDERMASK OVER BARE COPPER,  
COLOR, GREEN LPI.
  - PLATING: GOLD IMMERSION BOTH SIDES.
  - SILKSCREEN: BOTH COMPONENT SIDES WITH  
WHITE NON-CONDUCTIVE INK.
- TOLERANCES:
  - WARPAGE: (0.075mm) MAX. ALONG THE LONGEST  
DIAG. AS PER IPC-TM650 METHOD 2.4.22.
  - ETCHING: +0.03mm, -0.3mm OF MASTER PATTERN.
  - REGISTRATION: 0.075mm MAX.
  - MASK THICKNESS: 0.025mm MIN., 0.075mm MAX.
  - HOLE PLATING: 25um +/- 3um.
- SIZE:
  - CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00 ARE PRIMARY DATUMS.
- HOLE DIAMETERS ARE POST-PLATED SIZES.
- DEBURR AND BREAK ALL SHARP EDGES.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE  
CODE.
- PLUG ALL VIA'S WITH NON-CONDUCTIVE EPOXY  
AND PLATE OVER. AND PLANARIZE FINAL SURFACE.

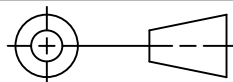
SIZE	QTY	SYM	PLATED	TOL
0.2	18	+	YES	+/-0.13
0.25	123	X	YES	+/-0.08
0.9	20	□	YES	+/-0.08
1.6	2	◇	YES	+/-0.13
2.41	14	⊗	YES	+/-0.13
4.75	4	⊗	NO	+/-0.13



L1-COMPONENT SIDE

UNLESS OTHERWISE SPECIFIED  
DIMENSIONS ARE IN MILLIMETERS  
TOLERANCES ON ANGLE ± 1°.XX ± 0.25  
.XXX ± 0.127

THIRD ANGLE PROJECTION



DO NOT SCALE DRAWING

CONTRACT NO.

APPROVALS

DATE

DRAWN

CHECKED

APPROVED

DESIGNED BY

05/04.18

03/11/19

03/11/19

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www.analog.com  
www.linear.comTITLE  
PCB FAB, LTM4668, TINY QUAD STEP-DOWN  
μMODULE™ REGULATOR

SIZE	CAGE CODE	DWG NO.	REV
A		DC2785A	3

SCALE 1/1 FILENAME: 2785A-3.PCB SHEET 1 OF 1