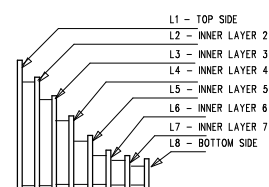


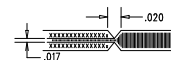
REVISION HISTORY			
ECD	REV	DESCRIPTION	DATE
-	1	PRODUCTION	07-26-21

#### LAYER STRUCTURE



#### NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062 +/- .005  
-TOTAL OF 8 LAYERS WITH 2 OZ. CU FOR ALL THE LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SNOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- DESIGN HAS SOLDER MASK DEFINED PADS ON U1-U4  
WITH 0.024 PAD, 0.020 SOLDER MASK, AND 0.019 SOLDER PASTE.  
DO NOT CHANGE ANY SIZE ON THIS COMPONENT.
- PCBS ARE TO BE ROHS COMPLIANT.
- SCORING FOR PANELIZED PCB (4 BDS/PANEL)



- PLUG ALL VIA'S WITH NON-CONDUCTIVE EPOXY AND PLATE OVER. PLANARIZE FINAL SURFACE.

SIZE	QTY	SYM	PLATED	TOL
0.008	1308	⊗	YES	+/-0.003
0.012	543	⊗	YES	+/-0.003
0.0315	2	⊕	YES	+/-0.003
0.035	37	⊕	YES	+/-0.003
0.037	26	⊕	YES	+/-0.003
0.055	10	⊕	YES	+/-0.003
0.064	25	⊕	YES	+/-0.003
0.07	2	⊗	NO	+/-0.003
0.187	4	⊕	NO	+/-0.003
0.189	10	⊕	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED		APPROVALS		ANALOG DEVICES		POWER BY LINEAR	
DIMENSIONS ARE IN INCHES		PCB DES.	LT	TITLE: FABRICATION DRAWING		FOR ADI CUSTOMER USE ONLY	
TOLERANCES:		APP ENG.	SG	HIGH CURRENT, POLYPHASE, STEP-DOWN POWER Module		www.Analog.com	
0.XX = ± 0.01				SUPPLY WITH DIGITAL POWER SYSTEM MANAGEMENT			
0.XXX = ± 0.005				SIZE IC NO. LT14700EY		REV	
INTERPRET DIM AND TOL PER ASME Y14.5M-1994				N/A DEMO CIRCUIT 2784B(A,B,C)		1	
THIRD ANGLE PROJECTION				SCALE = NONE		FILENAME: DC2784B-1PCB	
						SHT 1 OF 1	