

ADDITIONAL REQUIREMENT FOR PROTOTYPE FAB ONLY:

1. OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).

ADDITIONAL REQUIREMENTS FOR PRODUCTION FAB ONLY:

1. PROVIDE COMPLIANCE CERTIFICATES FOR ROHS, REACH AND CONFLICT-FREE MINERALS.

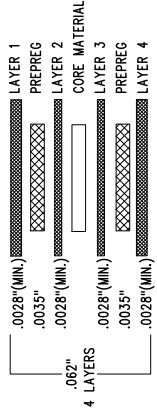
2. SOLDERABILITY BOARD WITH TEST RESULTS.

3. OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).

4. VACUUM PACKED WITH DESICCANT.

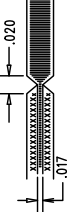
5. FULL PANEL WITH NO REJECT.

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. FINISHED CU ON ALL LAYERS
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
0.00" ARE DIMENSIONS AND TOLERANCES SHOWN.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER.
-FOR HOLE SIZE 0.003" VIAS HOLES NEED TO BE PLUGGED AND COVERED WITH SOLDERMASK.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
-DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
5. FINISH: -SNOBC USING LPI BOTH SIDES, COLOR GREEN.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE ROHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



9. DESIGN HAS SOLDER MASK DEFINED PADS ON UI. UI SOLDER MASK SIZE IS 0.4 MM AND PAD SIZE IS 0.5MM. DO NOT CHANGE ANY SIZE ON THIS COMPONENT.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XXX" = ±0.01" 0.XXXY" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION			
APPROVALS		ANALOG DEVICES	
PCB DES:	HZ	FOR ANI CUSTOMER USE ONLY	
APP ENG:	Charlie	TITLE: FABRICATION DRAWING	
		40V, 1.4A DUAL STEP-DOWN MODULE REGULATOR	
SIZE	IC NO.	REV	
N/A	LTM8078	1	
SCALE = NONE		FILENAME: DC2777A.PCB	
		SHT 1 OF 1	