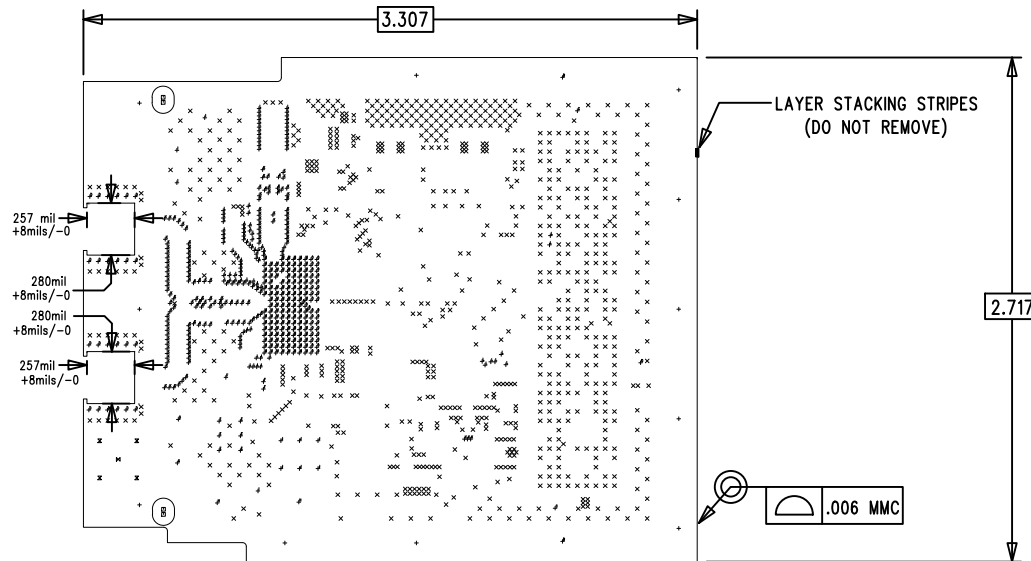


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE

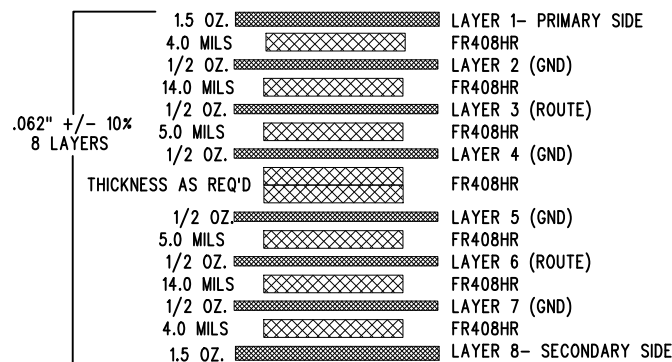
NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL:
 - PCB'S SHALL BE RoHS COMPLIANT.
 - MATERIAL SHALL BE FR408HR OR EQUIVALENT.
 - FINISHED THICKNESS SHALL BE 0.062" \pm 10%.
 - TOTAL OF 8 LAYERS, FINISHED COPPER THICKNESS AS PER FIG.1, "LAYER STRUCTURE".
 - FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
- DRILLING:
 - 4.1 DRILL HOLES PER SCHEDULE. HOLE PLATING SHALL BE 1.0 MILS THICK MINIMUM.
 - 4.2 ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
 - 4.3 HOLE LOCATION TOLERANCE FOR ALL HOLES SHALL BE 6 MILS DIAMETER TRUE POSITION.
- FINISH:
 - 5.1 LPI SOLDERMASK BOTH SIDES, COLOR GREEN.
 - 5.2 ENIG FINISH BOTH SIDES IN ACCORDANCE WITH IPC-4552.
 - 5.3 SILKSCREEN LEGENDS SHALL BE WHITE NON-CONDUCTIVE EPOXY INK.
- FABRICATOR SHALL NOT ADD LOGO OR DATE CODE TO ARTWORK. FABRICATOR MAY ADD SERIAL NUMBERS ON SECONDARY SIDE SILKSCREEN.
- BOARDS SHALL BE PANELIZED USING ROUTE AND RETAIN METHOD.
- BOARD USES FIXED IMPEDANCE DESIGN GEOMETRIES AS FOLLOWS:
 - 8.1 SURFACE MICROSTRIP PRIMARY SIDE WITH COPLANAR GND PLANE, 33 MILS WIDE, IMPEDANCE 50 OHMS \pm 10% AT 2Ghz, REFERENCE PLANE LAYER 3.
 - 8.2 EDGE COUPLED ASYMMETRICAL STRIPLINE, LAYERS 3 AND 6. DIFF. IMPEDANCE SHALL BE 100 OHMS AT 2Ghz. TRACES DESIGNED TO 4.2 MIL WIDTH/ 5.8 MIL GAP.
- SPECIAL VIA REQUIREMENTS
 - 9.1 ALL 12.0 MIL DIA. VIAS, QTY 24, AND 6.1 MIL DIA. VIAS, QTY 128, SHALL BE IPC 4761 TYPE VII FILLED AND CAPPED VIAS. USE NON-CONDUCTIVE FILLER.
 - 9.2 NOTE BLIND VIAS, 6.2 MIL DIA., QTY. 37, LAYER 1 TO LAYER 3, SHALL BE IPC 4761 TYPE VII FILLED AND CAPPED VIAS. USE NON-CONDUCTIVE FILLER.
- ELECTRICAL TEST STAMPS SHALL BE ON SECONDARY SIDE ONLY.





LINEAR TECHNOLOGY
DC2203A * LTC2000IY-X
LTC2000 HIGH SPEED DAC FMC ADAPTOR
PCB #DC2303A REV02

FIG.1 STACKUP DETAILS



SIZE	QTY	SYM	PLATED	THR/PRTL	TOL
86.61	11	+	YES	THR	\pm 3 MILS
10	889	X	YES	THR	\pm 3 MILS
118.11 x 145.67	2	□	NO	THR	\pm 6 MILS
35	6	+	YES	THR	\pm 3 MILS
67	4	X	YES	THR	\pm 3 MILS
59	1	+	YES	THR	\pm 3 MILS
135	2	+	YES	THR	\pm 3 MILS
50	2	+	NO	THR	\pm 3/-0 MILS
12	24	+	YES	THR	\pm 3 MILS
20	3	+	YES	THR	\pm 3 MILS
65	2	+	YES	THR	\pm 3 MILS
95	2	+	YES	THR	\pm 3 MILS
40	4	+	YES	THR	\pm 3 MILS
30	6	+	YES	THR	\pm 3 MILS
8	140	+	YES	THR	\pm 3 MILS
6	98	+	YES	THR	\pm 3 MILS
6.1	128	+	YES	THR	\pm 3 MILS
6.2	37	+	YES	P1-3	\pm 3 MILS

UNLESS OTHERWISE SPECIFIED		APPROVALS		<div> LINEAR TECHNOLOGY</div> <div>1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY</div>	
DIMENSIONS ARE IN INCHES		PCB DES.	M.HAWKINS		
TOLERANCES:		APP ENG.	C.MAYOTT	TITLE: FABRICATION DRAWING LTC2000 HIGH SPEED DAC FMC ADAPTER	
0.XX" = ± 0.01"					
0.XXX" = ± 0.005"				SIZE IC NO. LTC2000IY-X N/A DEMO CIRCUIT 2303A	
INTERPRET DIM AND TOL PER ASME Y14.5M-1994					
THIRD ANGLE PROJECTION				DWG. REV. 02	
		SCALE = NONE			
				SHT 1 OF 1	