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ADI
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REVISIONS			
ZONE	REV	DESCRIPTION	DATE
	1	ENGINEERING RELEASE	

NOTES: (UNLESS OTHERWISE SPECIFIED)

1. INTERPRET DRAWING IN ACCORDANCE WITH ASME Y14.5M-1994 DIMENSIONING AND TOLERANCING.
2. ALL FABRICATION AND PLATING PER IPC-A-600, CLASS 2. THE MINIMUM DIELECTRIC THICKNESS TO BE 0.0035 AS INTERPRETED BY IPC-A-600.

3 MATERIAL: PLASTIC SHEET LAMINATE TYPE FR4 .062+/- .005 THICK. 1/2oz COPPER CLAD START, ALL LAYERS. FINISHED PLATING THICKNESS TO BE 1 1/2oz COPPER MINIMUM, OUTSIDE LAYERS ONLY.

MATERIALS MUST MEET:

- IPC-L-108B SPECIFICATIONS FOR THIN METAL CLADs. BASE MATERIALS.
- ANSI/IPC-L-115B SPECIFICATIONS FOR RIGID METAL CLAD BASE MATERIALS.

4. ALL HOLES SHALL HAVE A 0.0012" MINIMUM COPPER BARREL WALL THICKNESS. PLATING IN ISOLATED AREAS NOT LESS THAN 0.0008", MINIMUM. HOLES SIZES ARE AFTER PLATING.

5. SOLDER MASK OVER BARE COPPER (SMOBC) BOTH SIDES USING A ADI APPROVED LIQUID-PHOTO-IMAGEABLE MASK (LPI), COLOR GREEN.

6. FINISH TO BE ELECTROLESS NICKEL IMMERSION GOLD (ENIG) PER IPC-4552. THE SOLDERABILITY OF EACH LOT SHALL BE VERIFIED IN ACCORDANCE WITH J-STD-003, CLASS 3.

7. ALL NON-DIMENSIONED HOLES SHALL BE WITHIN 0.003", OF HOLE CENTERS.

8. LAYER-TO-LAYER REGISTRATION SHALL BE WITHIN 0.003", ANY DIRECTION.

9. REMOVE ALL BURRS AND SHARP EDGES AFTER FUSING AND PLATING.

10. SILKSCREEN LEGEND SHALL BE APPLIED WITH A NON-CONDUCTIVE, WHITE EPOXY INK. LEGEND NOT PERMITTED ON SOLDERABLE LANDS.

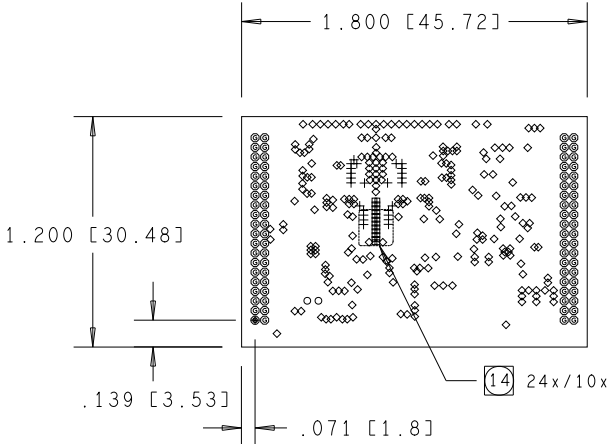
11. VENDOR LOGO AND DATE CODE MARKING REQUIRED BACKSIDE ONLY.

12. USE ADI SUPPLIED ELECTROINIC ARTWORK AND DATA.

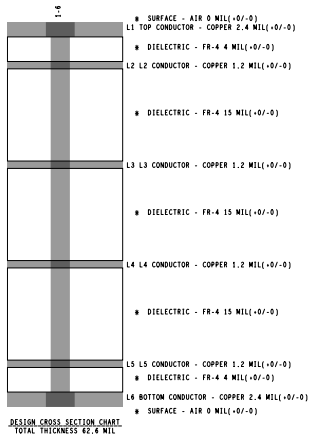
13. ARTWORK MODIFICATIONS ARE PROHIBITED WITHOUT WRITTEN AUTHORIZATION FROM ADI. GLOBAL MICROMODIFICATION OF PHOTO-TOOLING TO COMPENSATE FOR MANUFACTURING PROCESSES ARE ACCEPTABLE WITHIN MASTER DRAWING SPECIFICATIONS.

14 5 MIL DRILL (24) & 8 MIL DRILL (10). FINISH PLANARIZED AND PLATED OVER WITH COPPER AND SURFACE FINISH. PAD TO PAD CO-PLANARITY TO BE +/- .05MM OPTIONAL FILL WITH PETERS PP-2795 OR SAN-EIKAGAKU PHP-900 OR EQUIVALENT. MAINTAIN NO VOID PAD, 100% SOLIDS FILL MATERIAL.

15 NOTE DIELECTRIC THICKNESS CROSS SECTION DETAIL L1-L2 & L5-L6 TO BE 3.5 MIL TO 4 MIL MAXIMUM



HOLE CHART					
ALL UNITS ARE IN INCHES					
FIGURE	SIZE	TOLERANCE	PLATING	QTY	
14	□	0.005	+ .000 / - .005	PLATED	24
	+	0.008	+ .003 / - .003	PLATED	25
	◇	0.01	+ .003 / - .003	PLATED	122
	◇	0.01	+ .003 / - .003	PLATED	121
	○	0.014	+ .003 / - .003	PLATED	2
	●	0.03	+ .003 / - .003	PLATED	80



		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES [MM] TOLERANCES ARE: FRACTIONS DECIMALS ANGLES ± 1/64 .XX ±01 ±.5 DEG MATERIAL FINISH DO NOT SCALE DRAWING	CONTRACT NO. DRAWN JWOLKE CHECK ENG O.A. MFG	DATE 12JUN07 ELECTRONIC APPROVAL	ANALOG DEVICES 3550 North First Street SAN JOSE, CA 95134
NEXT ASSY	USED ON				FABRICATION DRAWING ADP5056 POWER BOARD
APPLICATION					SIZE D FSCM DWG NO. 08-060193 REV A
					SCALE NONE SHEET 1 OF 1