

Component Definition for: "ADIN2299_PRELIM"	
Number of Pins: 194	Number of Gates: 2
Name of Pattern: CSPBGA38X38_MODULE	Number of pattern Pins: 194
Symbol gating Information:	
Symbol name for gate 1 is ADIN2299_PRELIM_MODULE_B	
Symbol name for gate 2 is ADIN2299_PRELIM_MODULE_A	

Pin Data:		
Pin Number	Pin Name	Gate Number
A2	NC	2
A3	GND	2
A4	ETH0_RXD0	1
A5	ETH0_REFCLK	1
A6	ETH0_TXD0	1
A7	UART0_TX/ BUSY	1
A8	CAN1_TX	1
A9	SPI0_CLK	1

Symbol Pin 121 1 24 22 27 15 54 18

Component Definition for: "ADIN2299_PRELIM"		
Pin Data:		
Pin Number	Pin Name	Gate Number
A10	NC	2
A11	JTG_TMS	1
A12	*JTG_TRST	1
A13	GND	2
A14	NC	2
A15	NC	2
A16	GND	2
B1	NC	2
B3	GND	2
B4	ETH0_RXD1	1
B5	ETH0_TXEN	1
B6	ETH0_TXD1	1
B7	UART0_RX	1
B8	CAN1_RX	1
B9	SPI0_MISO/SPI0_D1	1
B10	*SPI0_SEL3_SS	1

Symbol	Pin
	120
	8
	6
	2
	119
	118
	3
	117
	4
	23
	25
	26
	14
	55
	16
	19

Component Definition for: "ADIN2299_PRELIM"		
Pin Data:		
Pin Number	Pin Name	Gate Number
B11	JTG_TDO	1
B12	JTG_TDI	1
B13	GND	2
B14	NC	2
B15	*WP	1
B16	NC	2
B17	GPIO_8	1
C1	NC	2
C2	NC	2
C3	GND	2
C4	ETH0_ CRS	1
C5	ETH0_ MDIO	1
C6	ETH0_ MDC	1
C7	GND	2
C8	GND	2
C9	SPI0_ MOSI/SPI0_ D0	1

Symbol	Pin
	10
	7
	5
	116
	56
	115
	49
	114
	113
	6
	21
	28
	29
	7
	8
	17

Component Definition for: "ADIN2299_PRELIM"		
Pin Data:		
Pin Number	Pin Name	Gate Number
C10	GND	2
C11	JTG_TCK	1
C12	*INT_IN	1
C13	*RREQ	1
C14	NC	2
C15	NC	2
C16	GPIO_7	1
C17	GPIO_6	1
D1	GND	2
D2	GND	2
D3	GND	2
D4	GND	2
D5	GND	2
D14	GND	2
D15	GND	2
D16	GPIO_5	1

Symbol	Pin
	9
	9
	11
	20
	112
	111
	50
	51
	10
	11
	12
	13
	14
	15
	16
	52

Component Definition for: "ADIN2299_PRELIM"		
Pin Data:		
Pin Number	Pin Name	Gate Number
D17	GPIO_4	1
E1	NC	2
E2	NC	2
E3	GND	2
E4	GND	2
E15	NC	2
E16	SDONE	1
E17	LT2	1
F1	GND	2
F2	NC	2
F3	SCL	1
F15	NC	2
F16	LT1	1
F17	LT0	1
G1	SPI0_D2	1
G2	SPI0_D3	1

Symb Pin
53
110
109
17
18
108
13
30
19
107
33
106
31
32
35
36

Component Definition for: "ADIN2299_PRELIM"		
Pin Data:		
Pin Number	Pin Name	Gate Number
G3	SDA	1
G7	GND	2
G8	GND	2
G9	GND	2
G10	GND	2
G11	GND	2
G15	GND	2
G16	GND	2
G17	GND	2
H1	GND	2
H2	GND	2
H3	NC	2
H7	GND	2
H8	GND	2
H9	GND	2
H10	GND	2

Symbol	Pin
	34
	20
	21
	22
	23
	24
	25
	26
	27
	28
	29
	105
	30
	31
	32
	33

Component Definition for: "ADIN2299_PRELIM"		
Pin Data:		
Pin Number	Pin Name	Gate Number
H11	GND	2
H15	GND	2
H16	V_3V3_IN	1
H17	V_3V3_IN	1
J1	TM0_ TMR1	1
J2	TM0_ TMR2	1
J3	NC	2
J7	GND	2
J8	GND	2
J9	GND	2
J10	GND	2
J11	GND	2
J15	GND	2
J16	V_3V3_IN	1
J17	V_3V3_IN	1
K1	GND	2

Symb	Pin
	34
	35
	1
	2
	42
	41
	104
	36
	37
	38
	39
	40
	41
	3
	4
	42

Component Definition for: "ADIN2299_PRELIM"		
Pin Data:		
Pin Number	Pin Name	Gate Number
K2	GND	2
K3	NC	2
K7	GND	2
K8	GND	2
K9	GND	2
K10	GND	2
K11	GND	2
K15	GND	2
K16	GND	2
K17	GND	2
L1	DNC	2
L2	DNC	2
L3	NC	2
L7	GND	2
L8	GND	2
L9	GND	2

Symb	Pin
	43
	103
	44
	45
	46
	47
	48
	49
	50
	51
	88
	87
	102
	52
	53
	54

Component Definition for: "ADIN2299_PRELIM"		
Pin Data:		
Pin Number	Pin Name	Gate Number
L10	GND	2
L11	GND	2
L15	GND	2
L16	NC	2
L17	NC	2
M1	DNC	2
M2	DNC	2
M3	NC	2
M15	GND	2
M16	NC	2
M17	NC	2
N1	DNC	2
N2	DNC	2
N3	NC	2
N15	GND	2
N16	GND	2

Symb	Pin
	55
	56
	57
	101
	100
	86
	85
	99
	58
	98
	97
	84
	83
	96
	59
	60

Component Definition for: "ADIN2299_PRELIM"		
Pin Data:		
Pin Number	Pin Name	Gate Number
N17	GND	2
P1	GND	2
P2	GND	2
P3	GND	2
P4	GND	2
P14	GND	2
P15	NC	2
P16	*SYS_HWRST	1
P17	SYS_ BMODE0	1
R1	MOD_LED1	1
R2	NET_LED1	1
R3	NET_LED2	1
R4	GND	2
R5	NC	2
R6	NC	2
R7	GND	2

Symbol	Pin
	61
	62
	63
	64
	65
	66
	95
	5
	39
	73
	71
	70
	67
	94
	93
	68

Component Definition for: "ADIN2299_PRELIM"		
Pin Data:		
Pin Number	Pin Name	Gate Number
R8	GND	2
R9	NC	2
R10	GND	2
R11	GND	2
R12	NC	2
R13	NC	2
R14	GND	2
R15	*REM_RESET	1
R16	*SYS_RESOUT	1
R17	SYS_BMODE1	1
T1	MOD_LED2	1
T2	GND	2
T3	GND	2
T4	GND	2
T5	GND	2
T6	*P1_ACTIVITY	1

Symbol	Pin
	69
	92
	70
	71
	91
	90
	72
	57
	12
	40
	72
	73
	74
	75
	76
	68

Component Definition for: "ADIN2299_PRELIM"		
Pin Data:		
Pin Number	Pin Name	Gate Number
T7	TIMER5	1
T8	SYNC2	1
T9	GND	2
T10	TIMER1	1
T11	TIMER0	1
T12	*P2_ACTIVITY	1
T13	GND	2
T14	GND	2
T15	GND	2
T16	GND	2
T17	NC	2
U2	P1_RD_N	1
U3	P1_RD_P	1
U4	P1_TD_N	1
U5	P1_TD_P	1
U6	*P1_LINK_STATUS	1

Symbol	Pin
	44
	38
	77
	47
	48
	62
	78
	79
	80
	81
	89
	64
	65
	66
	67
	69

Component Definition for: "ADIN2299_PRELIM"		
Pin Data:		
Pin Number	Pin Name	Gate Number
U7	SYNC1	1
U8	TIMER6	1
U9	DNC	2
U10	TIMER3	1
U11	TIMER2	1
U12	*P2_LINK_STATUS	1
U13	P2_RD_N	1
U14	P2_RD_P	1
U15	P2_TD_N	1
U16	P2_TD_P	1

Symbol	Pin
	37
	43
	82
	45
	46
	63
	58
	59
	60
	61

Pin Mapping:	
Pad Number	Component Pin Reference
1	A2
2	A3
3	A4
4	A5
5	A6
6	A7
7	A8

Component Definition for: "ADIN2299_PRELIM"	
Pin Mapping:	
Pad Number	Component Pin Reference
8	A9
9	A10
10	A11
11	A12
12	A13
13	A14
14	A15
15	A16
16	B1
17	B3
18	B4
19	B5
20	B6
21	B7
22	B8
23	B9
24	B10
25	B11
26	B12
27	B13
28	B14
29	B15

Component Definition for: "ADIN2299_PRELIM"	
Pin Mapping:	
Pad Number	Component Pin Reference
30	B16
31	B17
32	C1
33	C2
34	C3
35	C4
36	C5
37	C6
38	C7
39	C8
40	C9
41	C10
42	C11
43	C12
44	C13
45	C14
46	C15
47	C16
48	C17
49	D1
50	D2
51	D3

Component Definition for: "ADIN2299_PRELIM"	
Pin Mapping:	
Pad Number	Component Pin Reference
52	D4
53	D5
54	D14
55	D15
56	D16
57	D17
58	E1
59	E2
60	E3
61	E4
62	E15
63	E16
64	E17
65	F1
66	F2
67	F3
68	F15
69	F16
70	F17
71	G1
72	G2
73	G3

Component Definition for: "ADIN2299_PRELIM"	
Pin Mapping:	
Pad Number	Component Pin Reference
74	G7
75	G8
76	G9
77	G10
78	G11
79	G15
80	G16
81	G17
82	H1
83	H2
84	H3
85	H7
86	H8
87	H9
88	H10
89	H11
90	H15
91	H16
92	H17
93	J1
94	J2
95	J3

Component Definition for: "ADIN2299_PRELIM"	
Pin Mapping:	
Pad Number	Component Pin Reference
96	J7
97	J8
98	J9
99	J10
100	J11
101	J15
102	J16
103	J17
104	K1
105	K2
106	K3
107	K7
108	K8
109	K9
110	K10
111	K11
112	K15
113	K16
114	K17
115	L1
116	L2
117	L3

Component Definition for: "ADIN2299_PRELIM"	
Pin Mapping:	
Pad Number	Component Pin Reference
118	L7
119	L8
120	L9
121	L10
122	L11
123	L15
124	L16
125	L17
126	M1
127	M2
128	M3
129	M15
130	M16
131	M17
132	N1
133	N2
134	N3
135	N15
136	N16
137	N17
138	P1
139	P2

Component Definition for: "ADIN2299_PRELIM"	
Pin Mapping:	
Pad Number	Component Pin Reference
140	P3
141	P4
142	P14
143	P15
144	P16
145	P17
146	R1
147	R2
148	R3
149	R4
150	R5
151	R6
152	R7
153	R8
154	R9
155	R10
156	R11
157	R12
158	R13
159	R14
160	R15
161	R16

Component Definition for: "ADIN2299_PRELIM"	
Pin Mapping:	
Pad Number	Component Pin Reference
162	R17
163	T1
164	T2
165	T3
166	T4
167	T5
168	T6
169	T7
170	T8
171	T9
172	T10
173	T11
174	T12
175	T13
176	T14
177	T15
178	T16
179	T17
180	U2
181	U3
182	U4
183	U5

Component Definition for: "ADIN2299_PRELIM"	
Pin Mapping:	
Pad Number	Component Pin Reference
184	U6
185	U7
186	U8
187	U9
188	U10
189	U11
190	U12
191	U13
192	U14
193	U15
194	U16

Symbol Name: ADIN2299\_PRELIM\_MODULE\_B

99

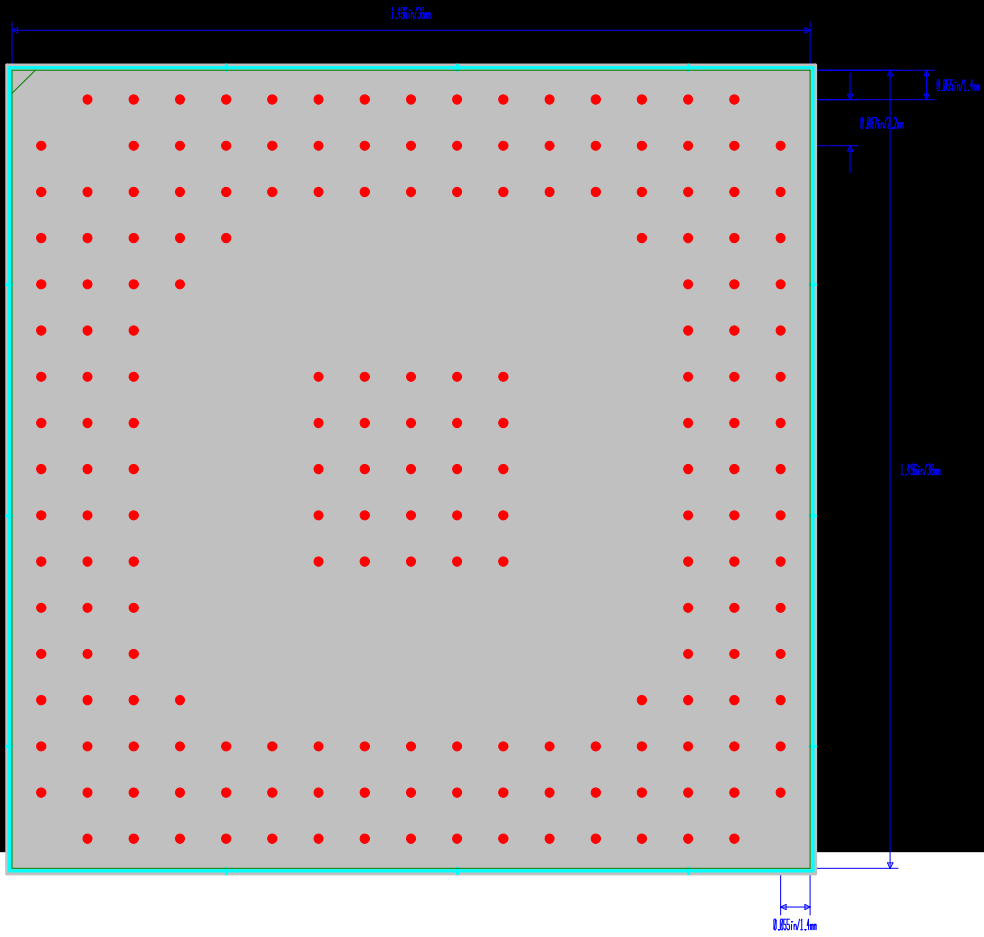
# Symbol Name: ADIN2299\_PRELIM\_MODULE\_A

Bus  
Type

1 of 2

- H16 V\_3V3\_IN
- H17 V\_3V3\_IN
- J16 V\_3V3\_IN
- J17 V\_3V3\_IN
- P16 +SYS\_HWRST
- A12 +JTG\_TRST
- B12 JTG\_TDI
- A11 JTG\_TMS
- C11 JTG\_TCK
- B11 JTG\_TDO
- C12 +INT\_IN
- R16 +SYS\_RESOUT
- F16 SDONE
- B7 UART0\_RX
- A7 UART0\_TX/ BUSY
- B9 SPI0\_MISO/SPI0\_D1
- C9 SPI0\_MOSI/SPI0\_D0
- A9 SPI0\_CLK
- B10 +SPI0\_SEL3\_SS
- C13 +RREQ
- C4 ETH0\_CRS
- A5 ETH0\_REFCLK
- B4 ETH0\_RXD1
- A4 ETH0\_RXD0
- B5 ETH0\_TXEN
- B6 ETH0\_TXD1
- A6 ETH0\_TXD0
- C5 ETH0\_MDIO
- C6 ETH0\_MDC
- F17 LT2
- F16 LT1
- F17 LT0
- F3 SCL
- G3 SDA
- G1 SPI0\_D2
- G2 SPI0\_D3
- U7 SYNC1
- T8 SYNC2
- P17 SYS\_BMODE0
- R17 SYS\_BMODE1

- MOD\_LED1 R1
- MOD\_LED2 T1
- NET\_LED1 R2
- NET\_LED2 R3
- #P1\_LINK\_STATUS U6
- #P1\_ACTIVITY T6
- P1\_TD.P U5
- P1\_TD.N U4
- P1\_RD.P U3
- P1\_RD.N U2
- #P2\_LINK\_STATUS U12
- #P2\_ACTIVITY T12
- P2\_TD.P U16
- P2\_TD.N U15
- P2\_RD.P U14
- P2\_RD.N U13
- #REM\_RESET R15
- #WP B15
- CAN1\_RX B8
- CAN1\_TX A8
- GPT0\_4 D17
- GPT0\_5 D16
- GPT0\_6 C17
- GPT0\_7 C16
- GPT0\_8 B17
- TIMER0 T11
- TIMER1 T10
- TIMER2 U11
- TIMER3 U10
- TIMER5 T7
- TIMER6 U8
- TMR0\_THR1 T1
- TMR0\_THR2 T2



Default Padstyle: EX.45Y.45007A

## Template Variables :

**Height = 138.189**

**MINHEIGHT = 0**

**RefDes = RefDes**

**Type = DEV**

**PN = PN**

**DEV = DEV**

**VAL = VAL**

**TOL = TOL**

**RefDes2 = RefDes2**

**TEMPLATE = ADI\_bga4.adt**

**NAME = CSPBGA38X38\_MODULE**

**CP = 86.61417**

**RP = 86.61417**

**C = 17**

**CRN = UL**

**PF = A1**

**L = ABCDEFGHJKLMNPRTUVWY**

**PO = A1**

**PC = 194**

Template Variables :

R = 17
S = A1A2
CHK = x1o15x1o1x1o37x8o8x10o6x11o6x3o5x3o6x3o5x3o6x3o5x3o6x3o5x3o6x3o5x3o6x1
AdiDefGridDocStyle = 1
PASTE_PATT = Standard
PSH = Round
SMOS = 0
SPOS = 0
PADS = 17.71654
DRL = 0
LEAD_SHAPE = Round
LEAD_SIZE = 0
LEAD_SIZE_Y = 0
NOT = 1
BODY_W = 1496.063
BODY_H = 1496.063
BODYOFFX = 0
BODYOFFY = 0
CHGT = 138.189

<b>Template Variables :</b>
-----------------------------

<b>CMHGT = 0</b>
------------------

<b>KO = 0</b>
---------------

<b>KOW = 160</b>
------------------

<b>KOH = 210</b>
------------------

<b>HTT = 0</b>
----------------

<b>CMARK = 0</b>
------------------

<b>HTSMOS = 0</b>
-------------------

<b>HTSPOS = 0</b>
-------------------

<b>HTW = 150</b>
------------------

<b>HTH = 200</b>
------------------

<b>A1_PSH = Oblong</b>
------------------------

<b>A1_SMOS = 0</b>
--------------------

<b>A1_SPOS = 0</b>
--------------------

<b>A1_PADW = 0</b>
--------------------

<b>A1_PADH = 0</b>
--------------------

<b>A1_DRL = 0</b>
-------------------

<b>A2_PSH = Oblong</b>
------------------------

<b>A2_SMOS = 0</b>
--------------------

<b>A2_SPOS = 0</b>
--------------------

<b>Template Variables :</b>
-----------------------------

<b>A2_PADW = 0</b>
--------------------

<b>A2_PADH = 0</b>
--------------------

<b>A2_DRL = 0</b>
-------------------

<b>FID = 0</b>
----------------

<b>PROT = 0</b>
-----------------

<b>REVLEVEL = A0</b>
----------------------

<b>3D = 1</b>
---------------

<b>MATP = Aluminum</b>
------------------------

<b>MATB = Black</b>
---------------------

<b>OX2 = 0</b>
----------------

<b>OY = 0</b>
---------------

<b>OZ = 0</b>
---------------

<b>OriginalPinCount = 194</b>
-------------------------------

