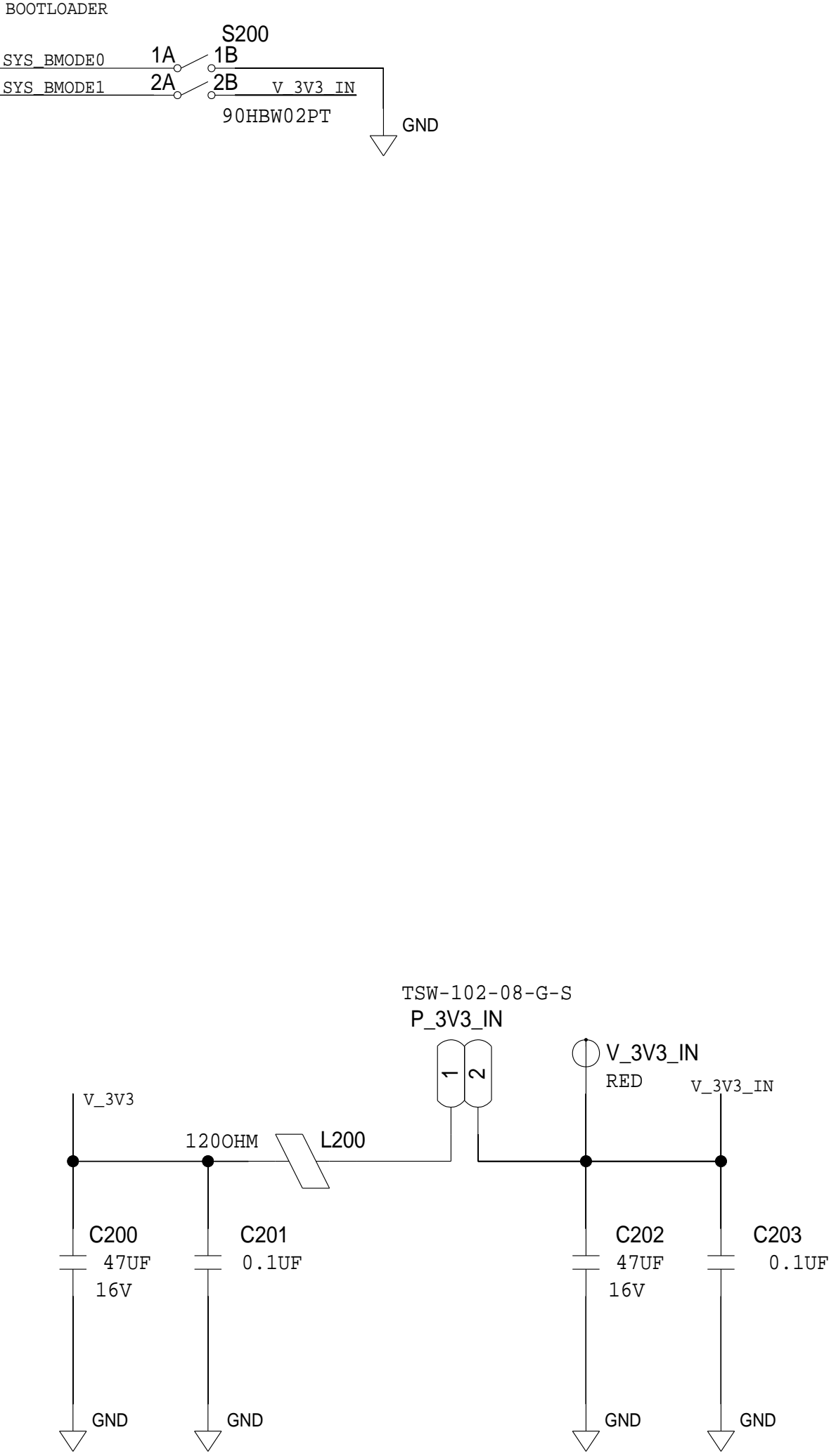
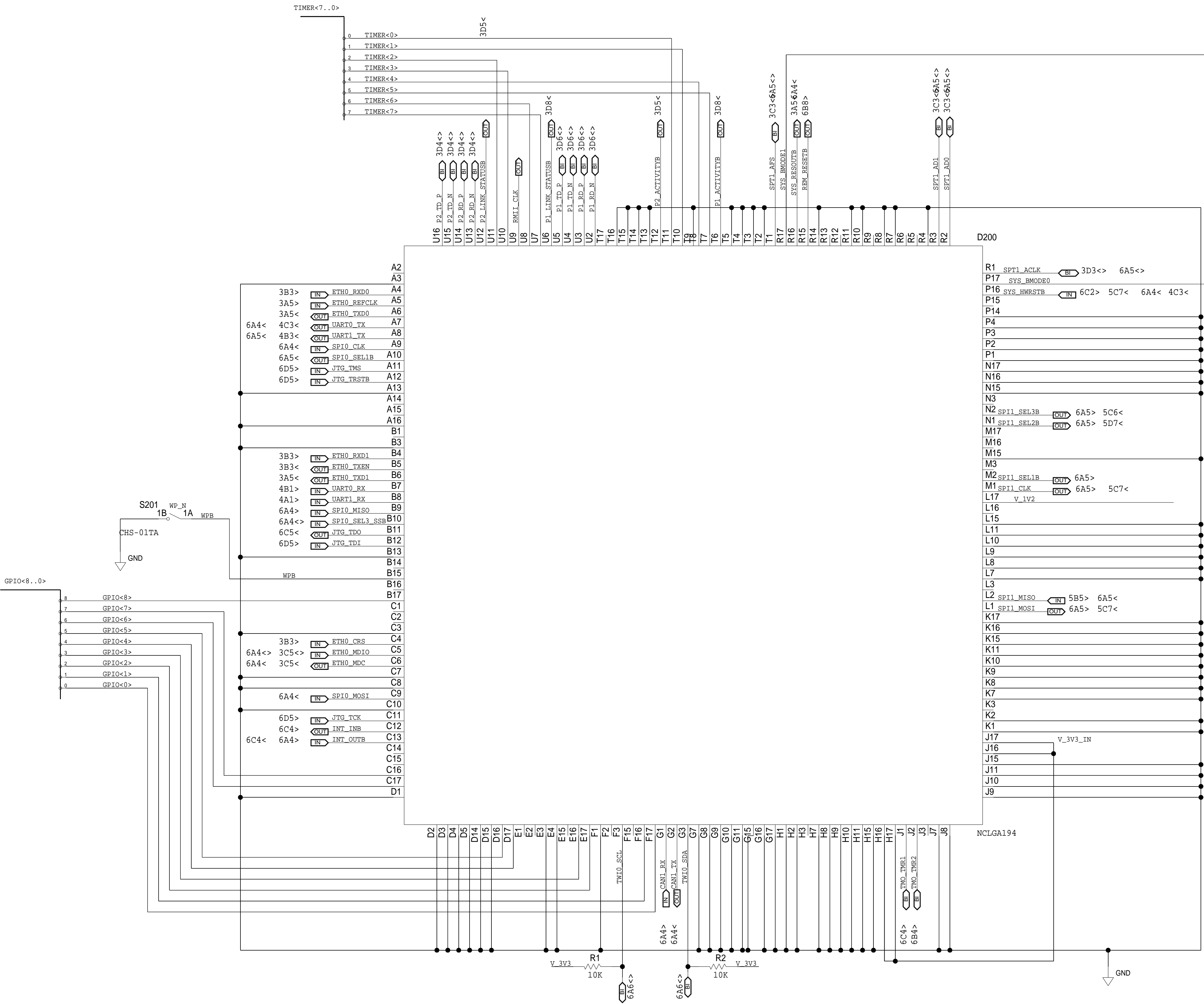


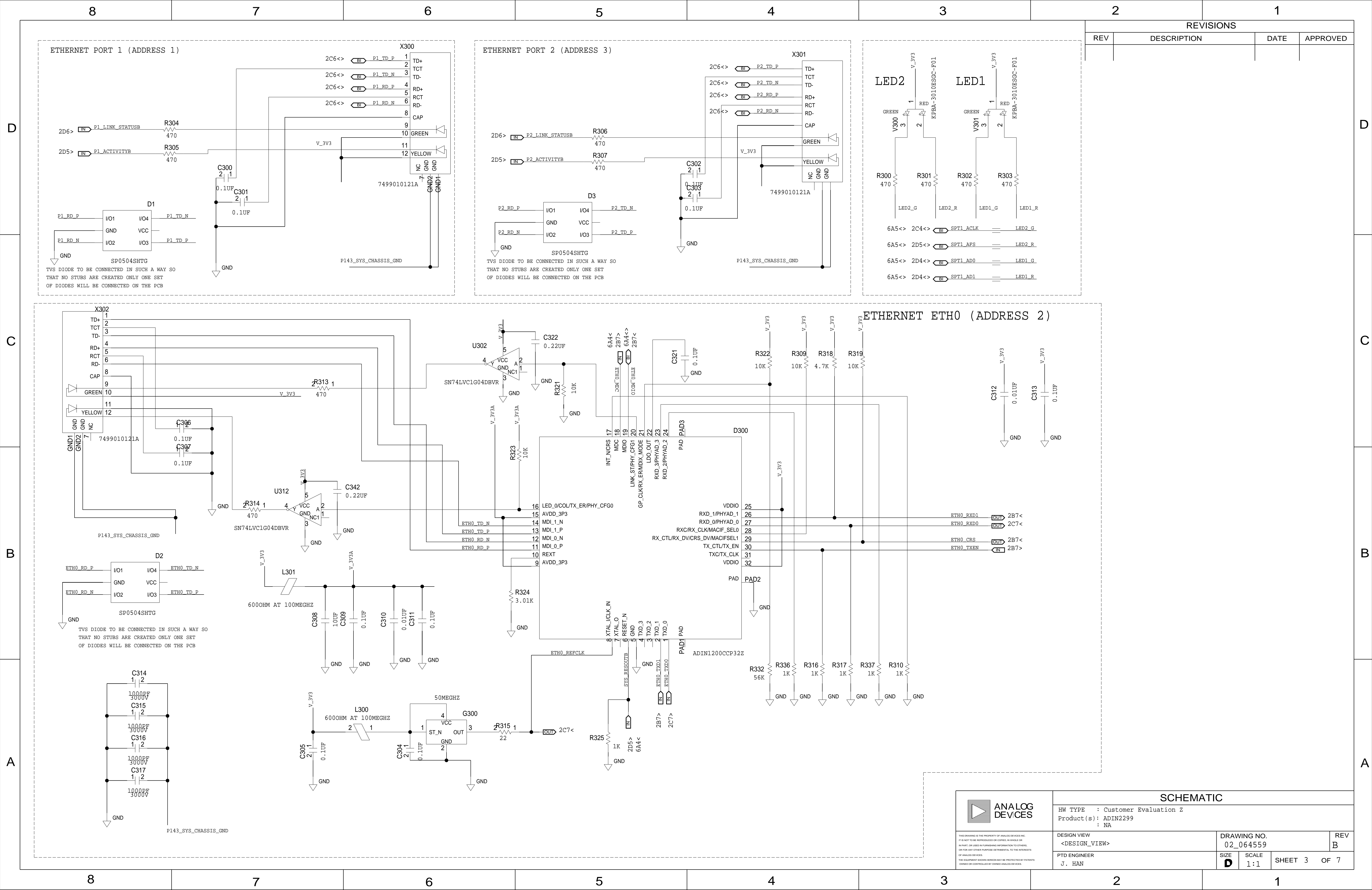
NGRP LGA MODULE (NGRP-ML)

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

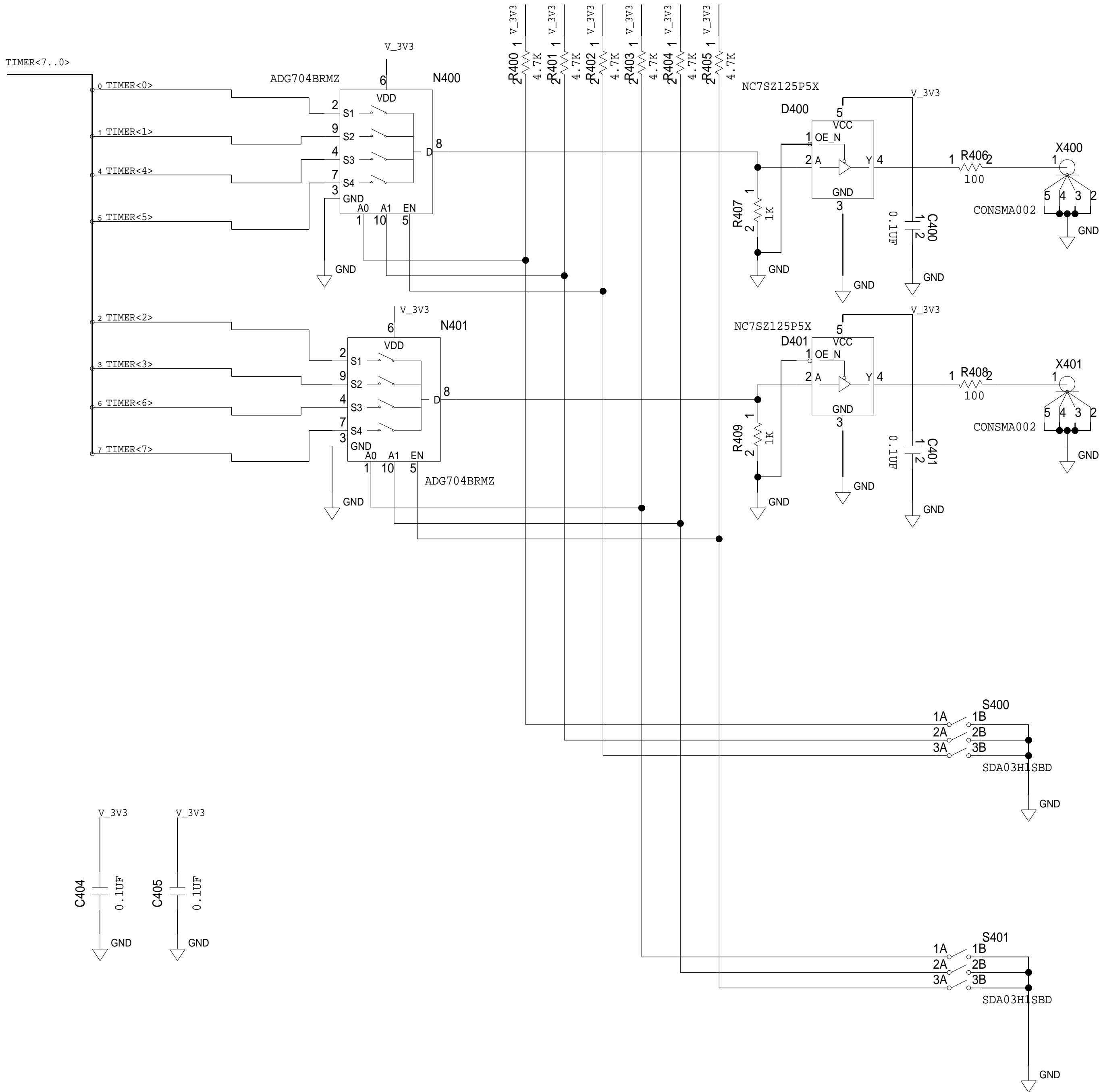


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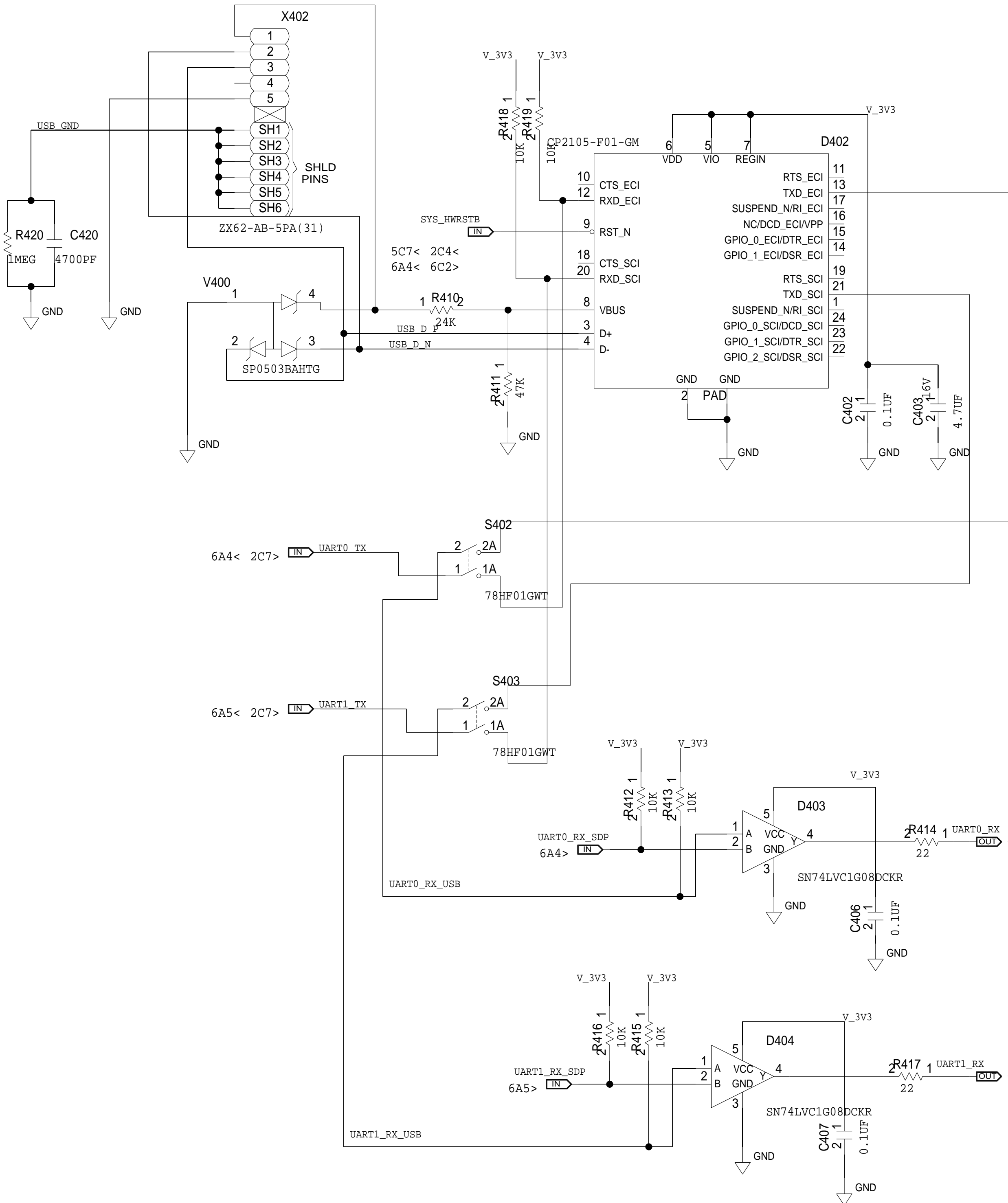
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DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_064559	REV B	
PTD ENGINEER J. HAN	SIZE D	SCALE 1:1	SHEET 2 OF 7



TIMER MUX AND SMA CONNECTORS



USB TO DUAL UART BRIDGE



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SCHEMATIC

HW TYPE : Customer Evaluation Z
Product(s): ADIN2299
: NA

DESIGN VIEW
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PTD ENGINEER
J. HAN

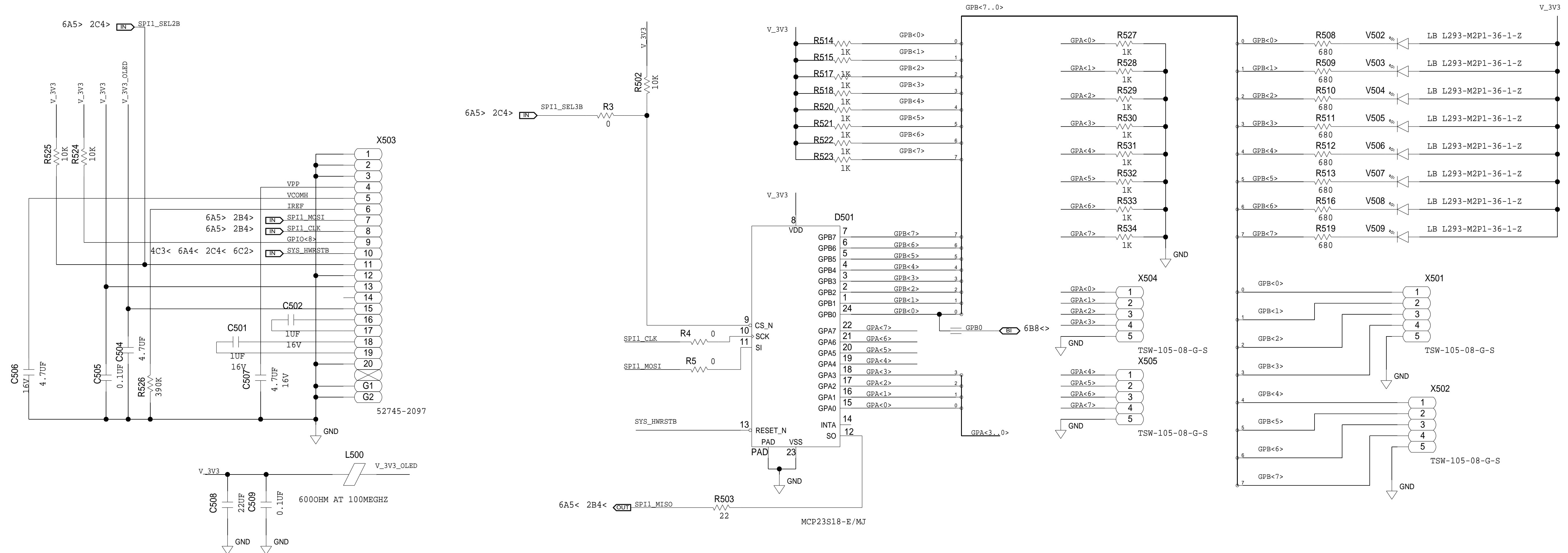
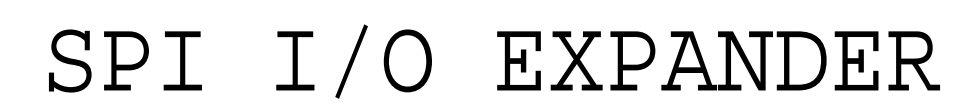
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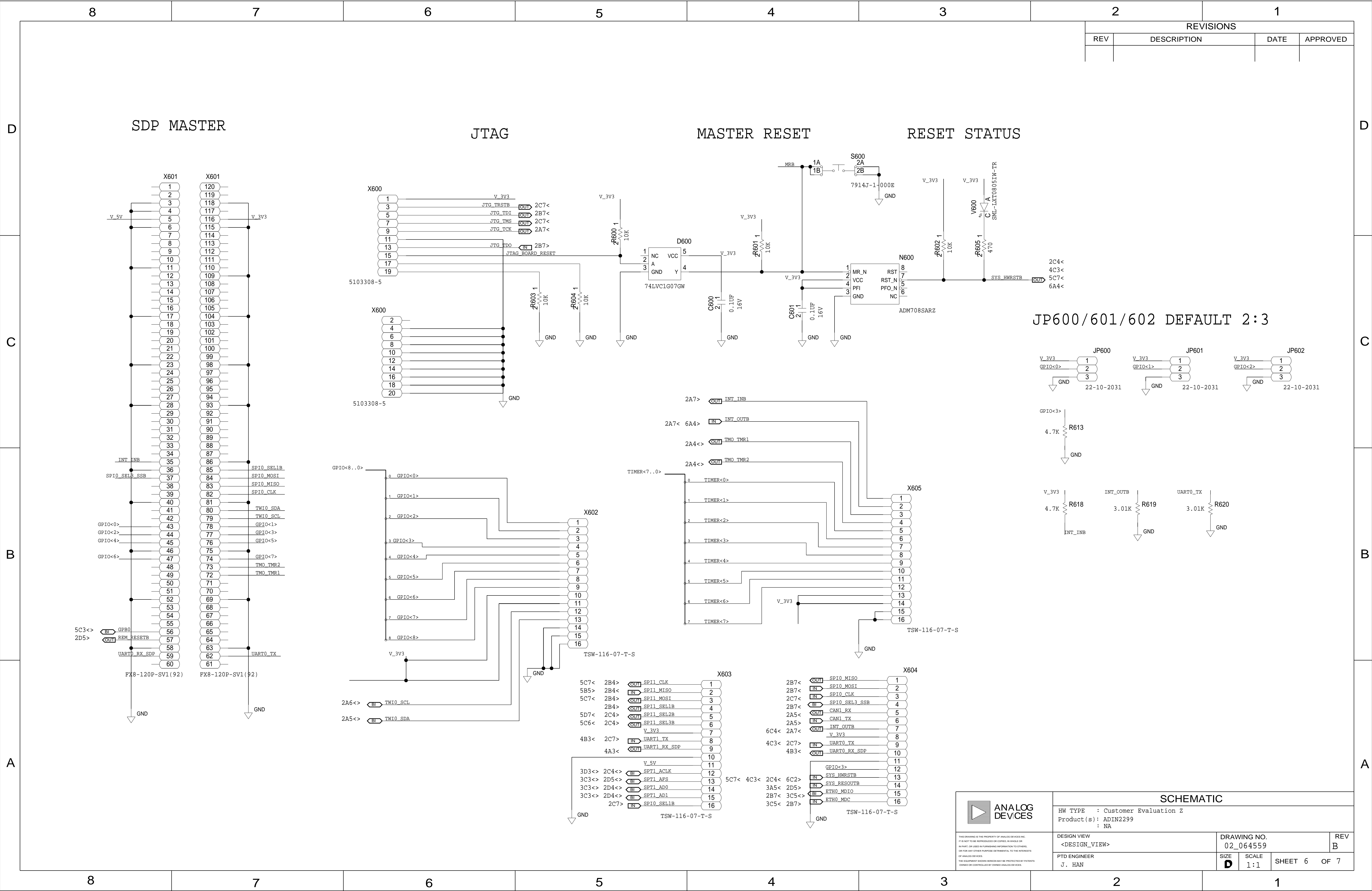
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SHEET 4 OF 7

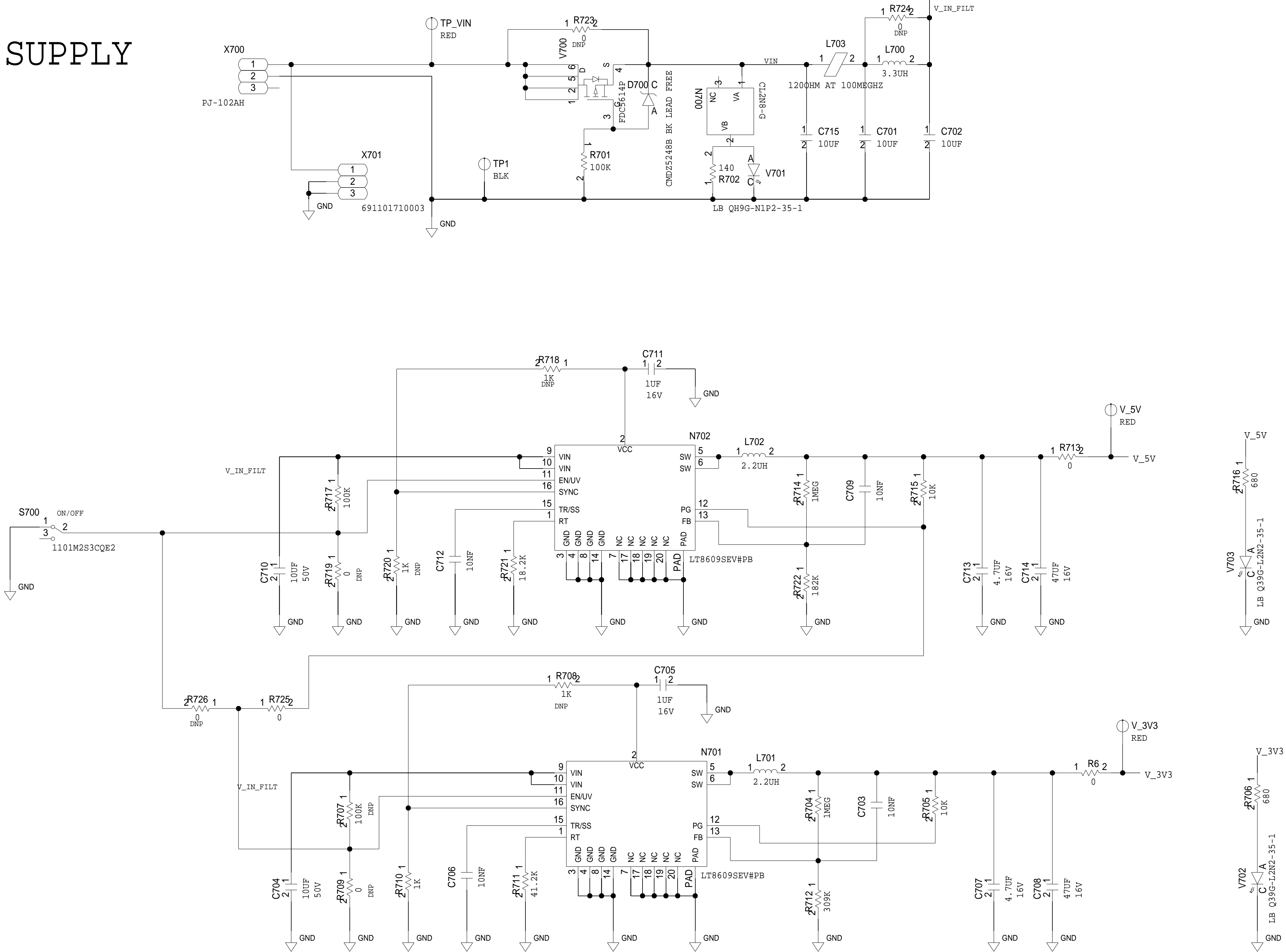
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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



POWER SUPPLY



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

HW TYPE : Customer Evaluation Z		DRAWING NO. 02_064559		REV B
Product(s) : ADIN2299		DESIGN VIEW <DESIGN_VIEW>		
PTD ENGINEER J. HAN		SIZE D	SCALE 1:1	SHEET 7 OF 7