

[illegible]











# UART UPPER PORT

REQUESTED FOR 4 PINS LOGICAL SYMBOL

# UART LOWER PORT

# SPI BUS LEVEL SHIFTER

# MINIQ USB HEADERS

# HIGH Z MODE UART TX COMPONENTS

## UPPER PORT

## LOWER PORT

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



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SCHEMATIC			
HW TYPE : Customer Evaluation Z Product(s) : ADES175X_EVKIT_A : NA			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_075909	REV A	
PTD ENGINEER P. SHARMA	SIZE D	SCALE 1:1	SHEET 4 OF 5



