

PCA SCH, MOTHERBOARD, CAMANCHE, MANAGER VERSION

Content:

- 1. Title Page
- 2. Eterna
- 3. Memory
- 4. Connectors, LED & Power

Notes:

1. Associated Documents



BOM
700-0216-0001 REV3



ASY DWG
705-0216-0001 REV1



PCB FAB
600-0216-0001 REV1

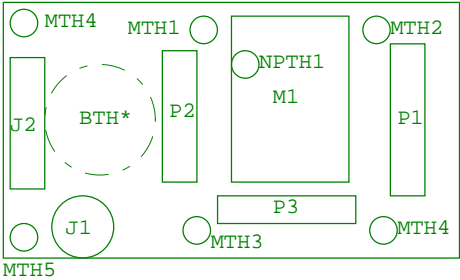
2. Assembly Options

- 1. External Memory or not
- 2. Russian or Canadian or uModule Oski based
- 3. PA_EXT_SET R value

Revision History:

Rev	Description	ECO	Author
01	Initial release	1212	Ric Peregrino
02	Add R25 for 256kB memory Load PB1, not PB2, adjust M1 PN connect CLI to P2 like Seymour	1262	Ric Peregrino
03	Update module symbol per datasheet	1310	Ric Peregrino
04	Load RADIO_INHIBIT pull down	1365	Christophe Niglio

Parts placement, not to scale



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CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND IS SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.

APPROVALS

DRAWN:

CHECKED:

APPROVED:

ENGINEER:

DESIGNER:



A Linear Technology Company

1630 McCarthy Blvd.
Milpitas, CA 95035

Phone: (408)432-1900
Fax: (408)434-0507

TITLE: **DC9018A-A**
PCA SCH, MOTHERBOARD, CAMANCHE

SIZE **B** DWG NO. **710-0216-0001**

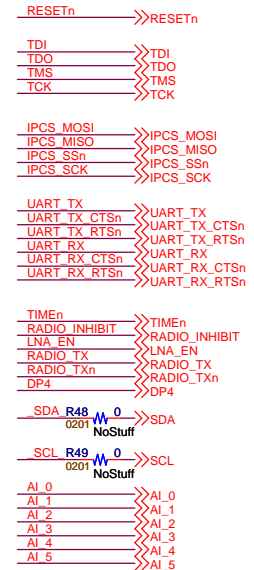
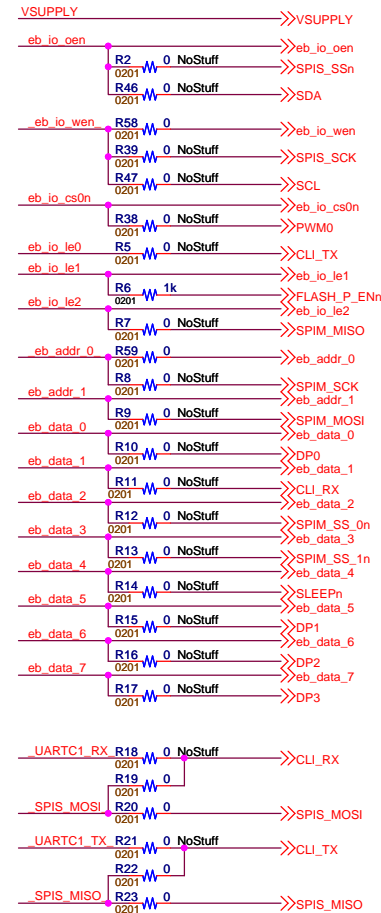
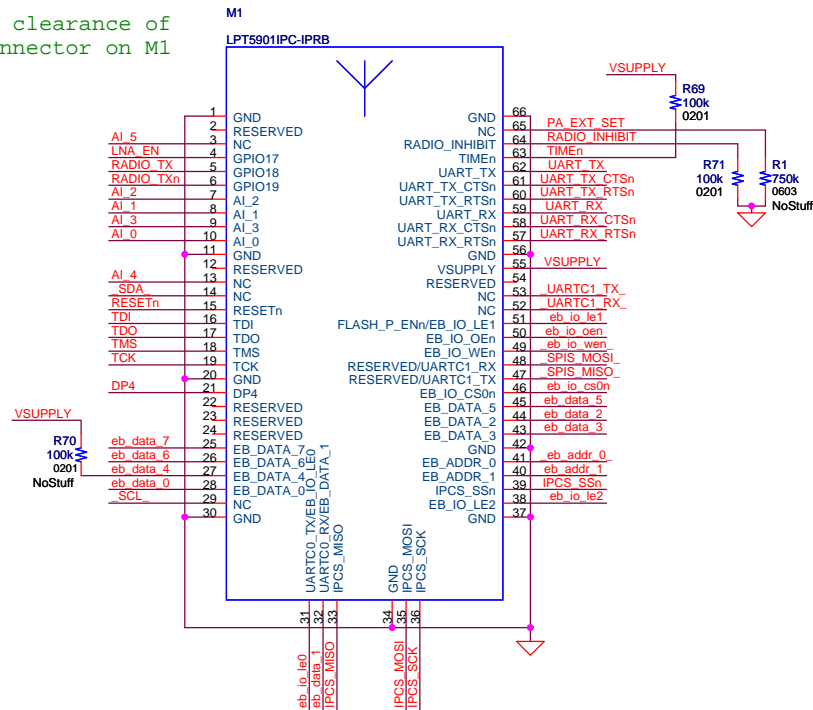
REV **04**

DATE: **Tuesday, February 25, 2014**

SHEET **1** OF **4**

NPTH1, for clearance of
antenna connector on M1

Place all R's on this page
as close as possible to M1



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TITLE: **DC9018A-A**

PCA SCH, MOTHERBOARD, CAMANCHE

SIZE
B

DWG NO. 710-0216-0001

REV
04

DATE: Tuesday, March 18, 2014

SHEET 2 OF 4

