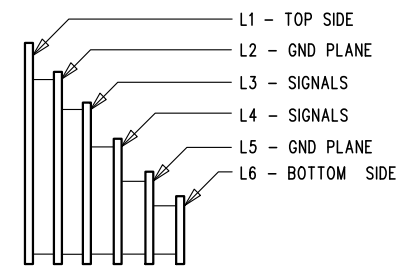


SIZE	QTY	SYM	PLATED	TOL
0.01	416	◇	YES	+/-0.003
0.035	22	⊗	YES	+/-0.003
0.055	10	+ <sup>B</sup>	YES	+/-0.003
0.067	25	⊗	YES	+/-0.003
0.07	2	⊗	NO	+/-0.003
0.187	4	+ <sup>A</sup>	NO	+/-0.005
0.189	2	+ <sup>A</sup>	YES	+/-0.005
0.205	2	□	YES	+/-0.005

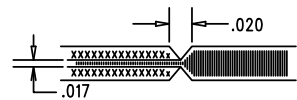
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	PRODUCTION	HW	02-28-18

### LAYER STRUCTURE



### NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062 +/- .005  
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ALL LAYERS  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0/0 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKScreens: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- DESIGN HAS SOLDER MASK DEFINED PADS ON U1.  
U1 SOLDER MASK SIZE IS 25 MIL AND PAD SIZE IS 29 MIL.  
DON'T CHANGE PAD SIZE OR SOLDER MASK SIZE.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX = ± 0.01 0.XXX = ± 0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION	APPROVALS		www.linear.com FOR ADI CUSTOMER USE ONLY	
	PCB DES.	LT	TITLE: FABRICATION DRAWING	
	APP ENG.	HW	7-BIT DAC CONTROLLED STEP-DOWN μMODULE REGULATOR	
			SIZE N/A	IC NO. LTC7106EDDB+LTM4636EY DC2830A
SCALE = NONE	FILENAME: DC2830A-1.PCB		REV 1	
			SHT 1 OF 1	