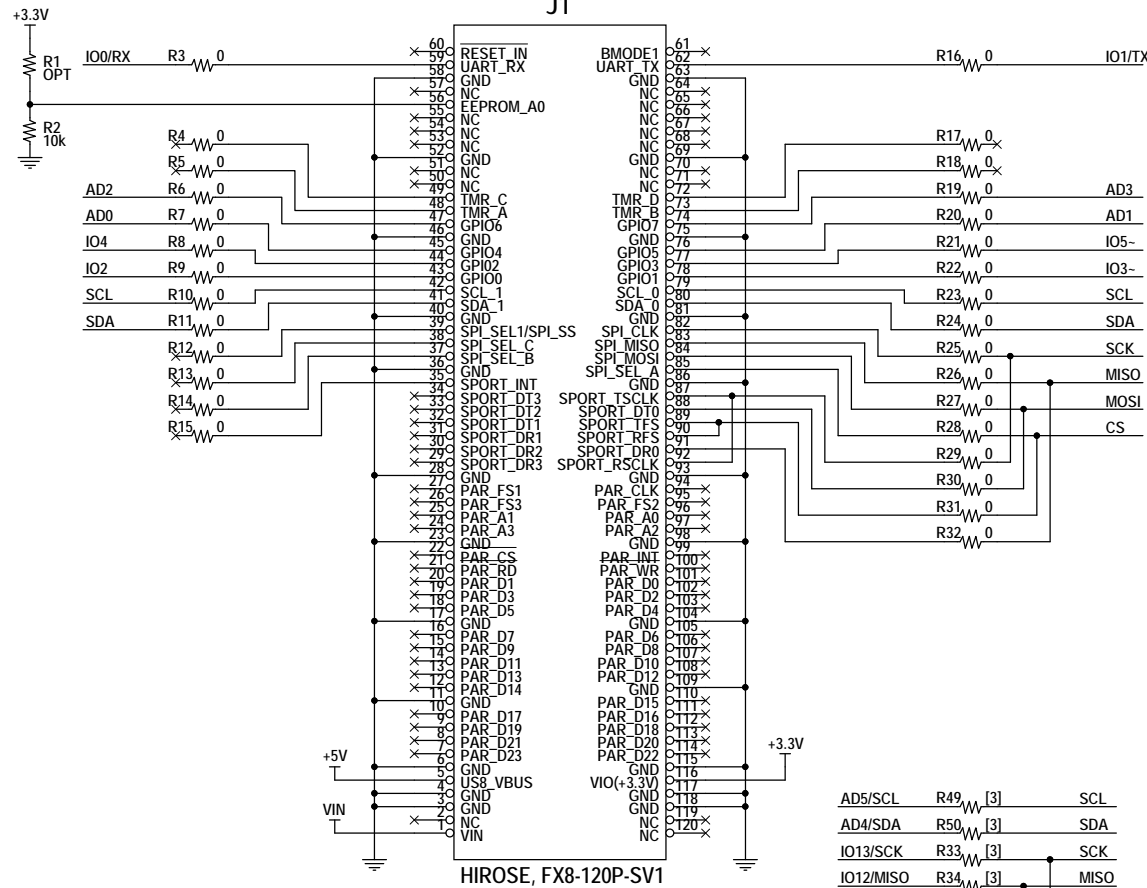
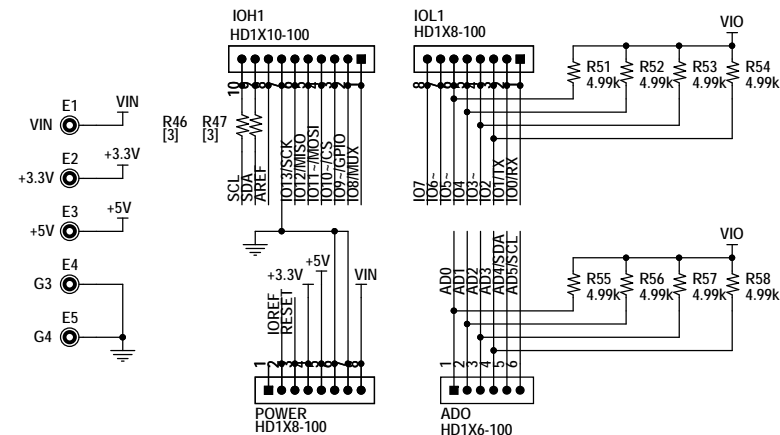


## SDP INTERFACE

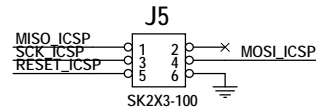
J1



HIROSE, FX8-120P-SV1

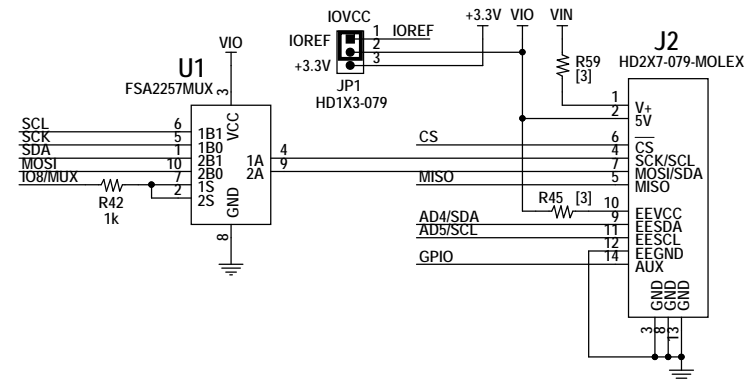
ARDUINO/LINDUINO/  
ADICUP360/ADICUP3095 INTERFACE

AD5/SCL	R49	[3]	SCL
AD4/SDA	R50	[3]	SDA
IO13/SCK	R33	[3]	SCK
IO12/MISO	R34	[3]	MISO
IO11-/MOSI	R35	[3]	MOSI
IO10-/CS	R36	[3]	CS
IO9-/GPIO	R37	[3]	GPIO
MISO_ICSP	R38	[3]	
MOSI_ICSP	R39	[3]	
SCK_ICSP	R40	[3]	
RESET_ICSP	R41	[3]	RESET

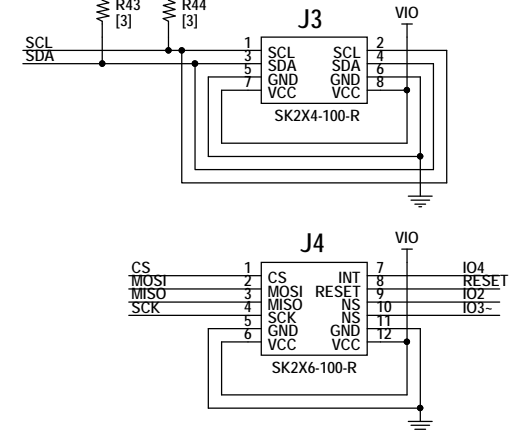


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	3	PRODUCTION	MARK T.	11-07-18

## QUIKEVAL INTERFACE



## PMODS



## NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS AND CAPACITORS ARE 0603.
2. INSTALL SHUNTS AS SHOWN.
3. ASSEMBLY TABLE:

BOARD FUNCTION	ASSY	J2		R33-R35, R49,R50	R36-R41,R45, R46,R47,R59	R43,R44
LINDUINO TO SDP, PMOD	A	SOCKET	BOT	OPT	OPT	OPT
X-DUINO TO LINDUINO, SDP, PMOD	B	HEADER	TOP	OPT	0	4.99K

## CUSTOMER NOTICE

ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER - SUPPLIED SPECIFICATIONS. HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE.

ADI CONFIDENTIAL - FOR CUSTOMER USE ONLY  
THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.

## APPROVALS

PCB DES. KIM T.

APP ENG. MARK T.

SCALE = NONE



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TITLE: SCHEMATIC

ARDUINO FORM FACTOR TO  
SDP/QUIKEVAL/PMOD ADAPTER

SIZE N/A

IC NO.

DEMO CIRCUIT 2741A

REV. 3

DATE: Wednesday, November 07, 2018

SHEET 1 OF 1