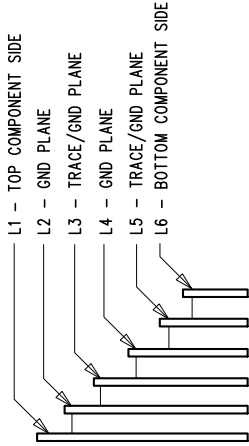
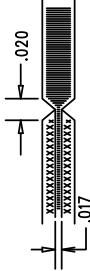


LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- 1. FAB PER IPC-A-600.
- 2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 6 LAYERS WITH 2 OZ. CU.
FOR INNER AND OUTER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- 3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- 4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
- 5. FINISH: -SNOBIC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- 6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- 7. PCBs ARE TO BE RoHS COMPLIANT.
- 8. SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
0.00984	153	+	YES	+/-0.003
0.187	4	X	YES	+/-0.003
0.09488	10	□	YES	+/-0.003
0.20512	4	◇	YES	+/-0.003
0.035	6	⊗	YES	+/-0.003
0.03543	3	⊗	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION

APPROVALS

PCB DES.	HZ
APP ENG.	YL
TITLE: FABRICATION DRAWING	
HIGH FREQUENCY POWER SUPPLY WITH GaN TRANSISTORS	
SIZE	IC NO.
N/A	LTC7800EUDC
SCALE = NONE	REV
	2
FILENAME: DC2736A-2	SHT 1 OF 1

LINEAR TECHNOLOGY

9330 MCCARTHY BLVD
MILPITAS, CA 95035
TEL: 408-591-5000
WWW.LTC.COM
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