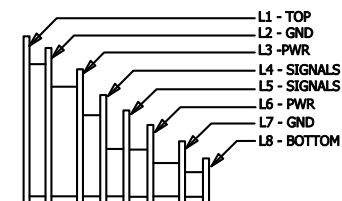


SIZE	QTY	SYM	PLATED	TOL
0.008	309	⊗	YES	+/-0.003
0.01	321	⊗	YES	+/-0.003
0.035	49	⊗	YES	+/-0.003
0.055	15	⊕ <sup>A</sup>	YES	+/-0.003
0.067	26	□	YES	+/-0.003
0.187	4	⊕	NO	+/-0.003
0.189	6	◇	YES	+/-0.003

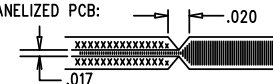
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	5	PRODUCTION	HZ	08-29-18

#### LAYER STRUCTURE


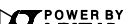



#### NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062 +/- .005  
-TOTAL OF 8 LAYERS WITH 2 OZ. CU ON ALL THE LAYERS  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- DESIGN HAS SOLDER MASK DEFINED PADS ON U1  
WITH 0.020 PAD, 0.016 SOLDER MASK, AND 0.015 SOLDER PASTE.  
DO NOT CHANGE ANY SIZE ON THIS COMPONENT.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



- PLUG ALL VIA'S WITH NON-CONDUCTIVE COPPER PASTE AND PLATE OVER. PLANARIZE FINAL SURFACE.

UNLESS OTHERWISE SPECIFIED		APPROVALS		  POWER BY LINEAR™ www.linear.com FOR ADI CUSTOMER USE ONLY	
DIMENSIONS ARE IN INCHES		PCB DES.	LT	TITLE: FABRICATION DRAWING	
TOLERANCES:		APP ENG.	HZ		
0.XX = ± 0.01				DUAL 50A OR SINGLE 100A uMODULE REGULATOR WITH DIGITAL POWER MANAGEMENT	
0.XXX = ± 0.005					
INTERPRET DIM AND TOL PER ASME Y14.5M-1994					
THIRD ANGLE PROJECTION				SIZE	IC NO.
				N/A	LTM4700EY DC2702A(A,B)
		SCALE = NONE		FILENAME:	REV
				DC2702A-5.PCB	5
					SHT 1 OF 1