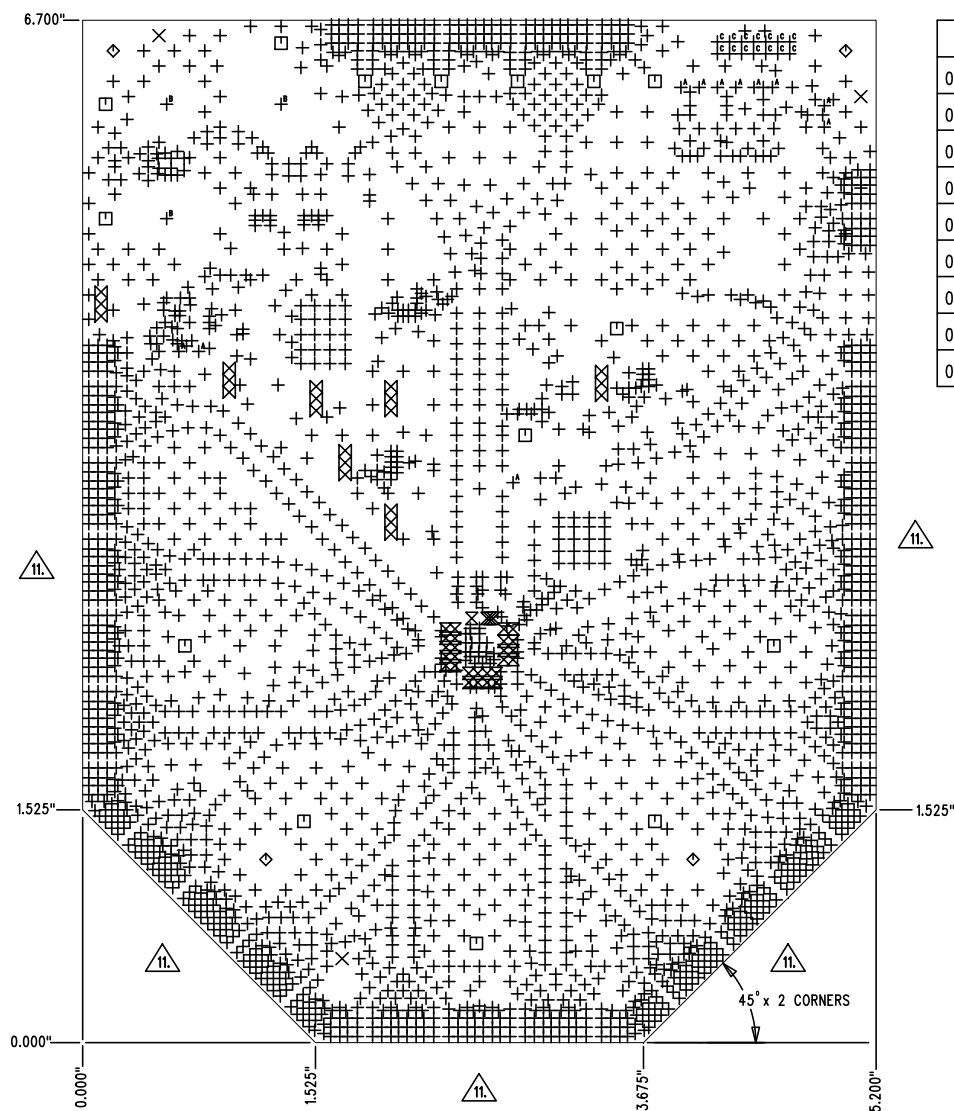


SHOWN FROM TOP SIDE

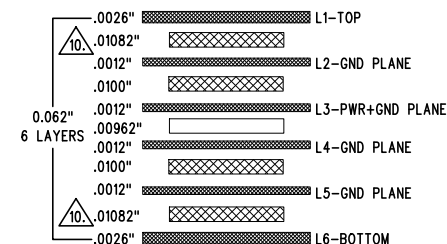


DRILL DRAWING LAYER
 LINEAR TECHNOLOGY DC2609A-2 LTC6952IUQG DATE: 07-05-17
 ULTRALOW JITTER PLL WITH 11 OUTPUTS AND JESD204B SUPPORT

SIZE	QTY	SYM	PLATED	TOL
0.012	2852	+	YES	+/-0.003"
0.07	3	×	NO	+/-0.003"
0.064	15	□	YES	+/-0.003"
0.19	4	◇	NO	+/-0.003"
0.006	30	⊗	YES	+/-0.003"
0.035	21	⊠	YES	+/-0.003"
0.025	11	+ ^A	YES	+/-0.003"
0.207	3	+ ^B	YES	+/-0.003"
0.04	14	+ ^C	YES	+/-0.003"

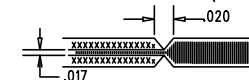
REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG. DATE
-	2	PRODUCTION	CHRIS P. 07-05-17


LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -EPOXY FIBERGLASS, NELCO 4000-13
 -FINISHED THICKNESS TO BE 0.062" +/- 0.005"
 -TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
 -FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
 0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
 -ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
 -HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
 -GOLD IMMERSION BOTH SIDES.
 -FOR SILKScreens: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):
- CONTROLLED 50 OHM +/-10% IMPEDANCE FOR LAYERS 1-2 LAYER 6-5, USING 0.02" TRACE.
- SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.
- INNER AND OUTER LAYER COPPER SHALL BE EXPOSED ALONG BOARD EDGES. DO NOT MODIFY INNER LAYER COPPER BACKOFF OUTLINE (SMA CONNECTOR).



UNLESS OTHERWISE SPECIFIED		APPROVALS		 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408) 432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
DIMENSIONS ARE IN INCHES		PCB DES.	KIM T.		
TOLERANCES:		APP ENG.	CHRIS P.	TITLE: FABRICATION DRAWING	
0.XX" = ±0.01"				ULTRALOW JITTER PLL WITH	
0.XXX" = ±0.005"				11 OUTPUTS AND JESD204B SUPPORT	
INTERPRET DIM AND TOL				SIZE	IC NO.
PER ASME Y14.5M-1994				N/A	LTC6952IUQG
THIRD ANGLE PROJECTION					REV
					2
		SCALE = NONE		FILENAME:	DC2609A-2.PCB
				SHT 1 OF 1	