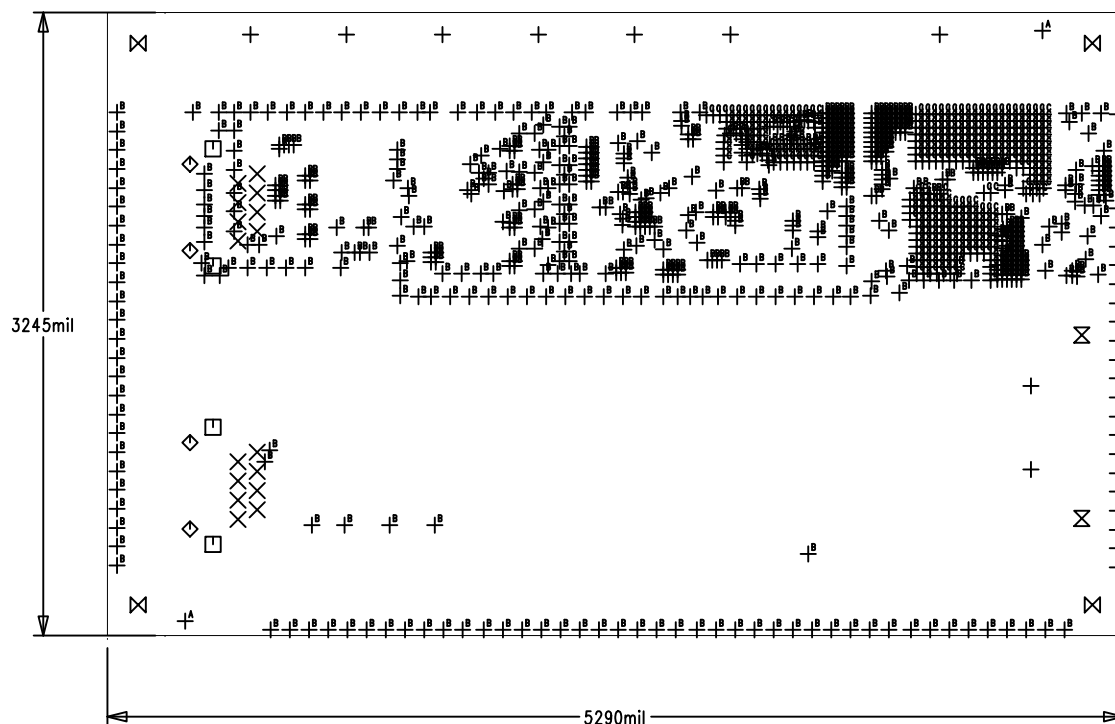


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	3	PRODUCTION	Ryan Huff	1-20-15

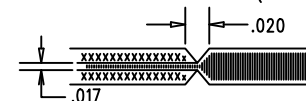
SHOWN FROM TOP SIDE



SIZE	QTY	SYM	PLATED	TOL
94	9	+	YES	+/-0.0
36	16	X	YES	+/-0.0
62	4	□	YES	+/-0.0
128	4	◇	NO	+/-0.0
205	2	⊗	YES	+/-0.0
188	4	⊗	YES	+/-0.0
70	2	+ ^A	NO	+/-0.0
10	739	+ ^B	YES	+/-0.0
20	382	+ ^C	YES	+/-0.0

NOTES: UNLESS OTHERWISE SPECIFIED


- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN. 0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



9. CONTROLLED DIELECTRIC FOR ALL LAYERS

LAYER STRUCTURE

.00270"	L1-TOP
.010" +/- 10%	
.00135"	L2-PWR/SIGNAL GND
.014" +/- 10%	
.00135"	L3-SIGNAL1
.0084" +/- 10%	
.00135"	L4-SIGNAL2
.014" +/- 10%	
.00135"	L5-PWR GND
.010" +/- 10%	
.00270"	L6-BOTTOM

UNLESS OTHERWISE SPECIFIED		APPROVALS		 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
DIMENSIONS ARE IN INCHES		PCB DES.	RYAN HUFF		
TOLERANCES:		APP ENG.	RYAN HUFF	TITLE: FABRICATION DRAWING HIGH EFFICIENCY LTPoE++ PD INTERFACE WITH INTEGRATED SWITCHING REGULATOR	
0.XX" = ±0.01"					
0.XXX" = ±0.005"				SIZE	
INTERPRET DIM AND TOL				IC NO.	
PER ASME Y14.5M-1994				LT4276AIUFD	
THIRD ANGLE PROJECTION				DEMO CIRCUIT 2262A	
				REV	
				3	
		SCALE = NONE		FILENAME: DC2262A - FORWARD.pcb	
				SHT 1 OF 1	