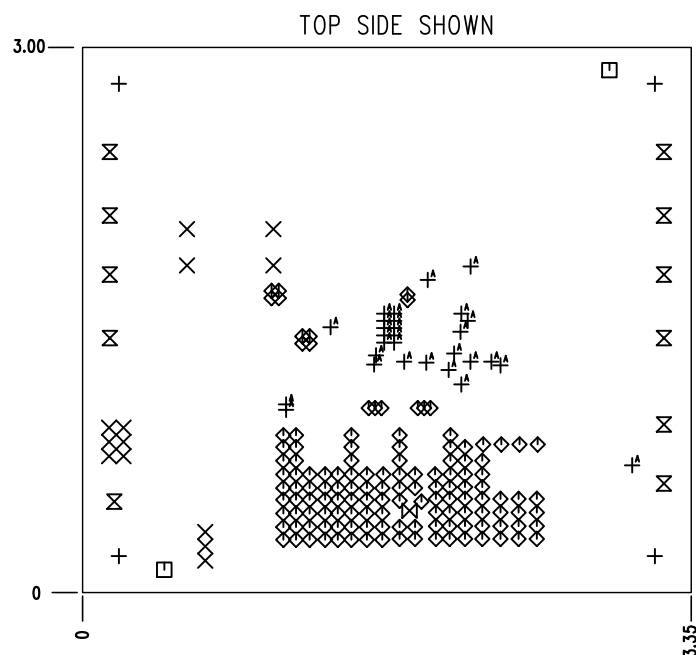
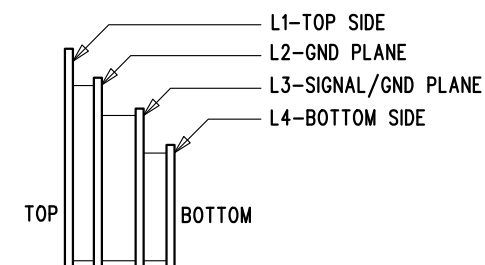


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	3	PRODUCTION	DB	01-09-18



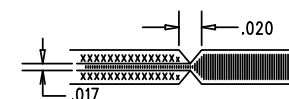
SIZE	QTY	SYM	PLATED	TOL
0.01	29	⊕ ^A	YES	+/-0.003
0.012	132	◇	YES	+/-0.003
0.035	13	×	YES	+/-0.003
0.04	1	⊠	YES	+/-0.003
0.07	2	□	NO	+/-0.003
0.095	11	⊗	YES	+/-0.003
0.187	4	⊕	YES	+/-0.005

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062+/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0/0 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED		APPROVALS		ANALOG DEVICES		POWER BY LINEAR™	
DIMENSIONS ARE IN INCHES		PCB DES.	LT			www.linear.com	
TOLERANCES:		APP ENG.	DB			FOR ADI CUSTOMER USE ONLY	
0.XX = ± 0.01						TITLE: FABRICATION DRAWING	
0.XXX = ± 0.005						150V, LOW Iq, STEP-DOWN SUPPLY	
INTERPRET DIM AND TOL PER ASME Y14.5M-1994						SIZE	IC NO.
THIRD ANGLE PROJECTION						N/A	LTC3894EFE
							DEMO BOARD 2506A
						REV	3
						FILENAME: DC2506A-3.PCB	SHT 1 OF 1