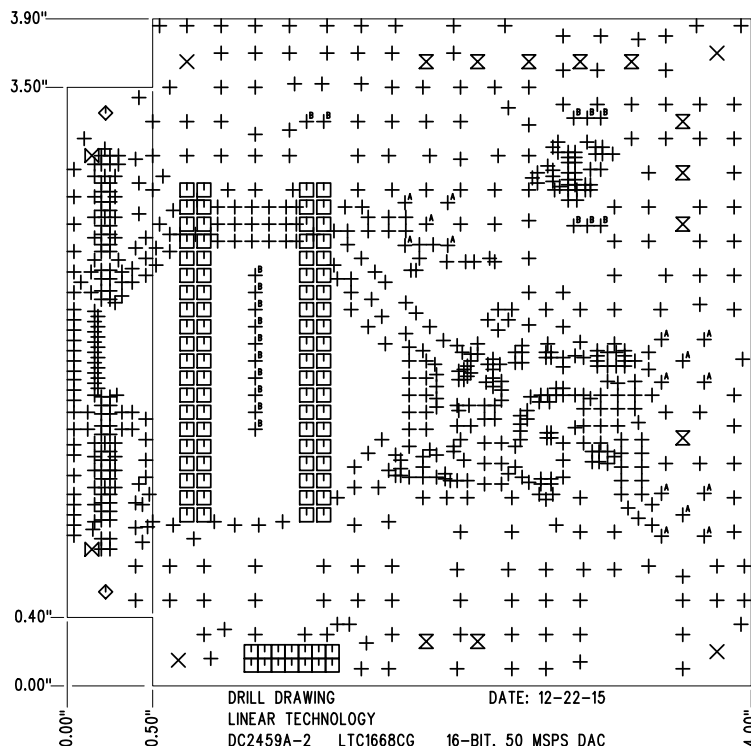


SHOWN FROM TOP SIDE

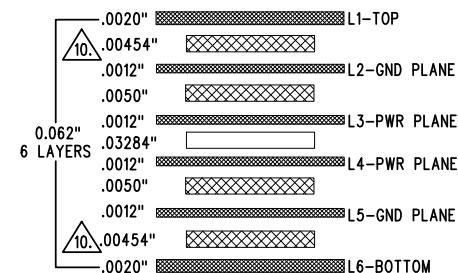
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	1ST PROTOTYPE	NOE Q.	12-22-15



DRILL DRAWING
LINEAR TECHNOLOGY
DC2459A-2 LTC1668CG 16-BIT, 50 MSPS DAC
DATE: 12-22-15

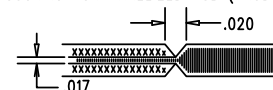
SIZE	QTY	SYM	PLATED	TOL
0.012	659	+	YES	+/-0.003"
0.19	4	X	NO	+/-0.003"
0.04	94	□	YES	+/-0.003"
0.125	2	◇	YES	+/-0.003"
0.094	11	⊗	YES	+/-0.003"
0.04	2	⊗ ^A	NO	+/-0.003"
0.055	15	+ ^A	YES	+/-0.003"
0.035	18	+ ^B	YES	+/-0.003"

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR BLACK.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



- CONTROLLED 50 OHM +/-10% IMPEDANCE FOR SINGLE ENDED STRIP LINE FOR LAYER 1-2 AND LAYER 6-5, USING 0.006" TRACE AT 2GHz FREQ.

△10. SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.

UNLESS OTHERWISE SPECIFIED		APPROVALS				1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408) 432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		PCB DES.	KIM T.				
		APP ENG.	NOE Q.			TITLE: FABRICATION DRAWING	
						16-BIT, 50 MSPS DAC	
						SIZE N/A	IC NO. LTC1668CG
						DEMO CIRCUIT 2459A	
						REV 1	
		SCALE = NONE				FILENAME: DC2459A-1.PCB	SHT 1 OF 1