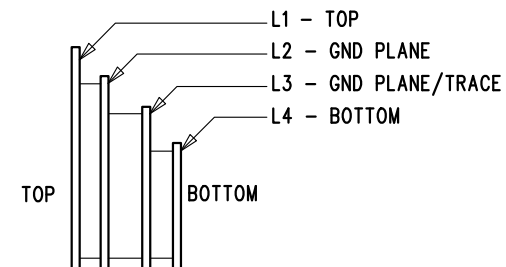
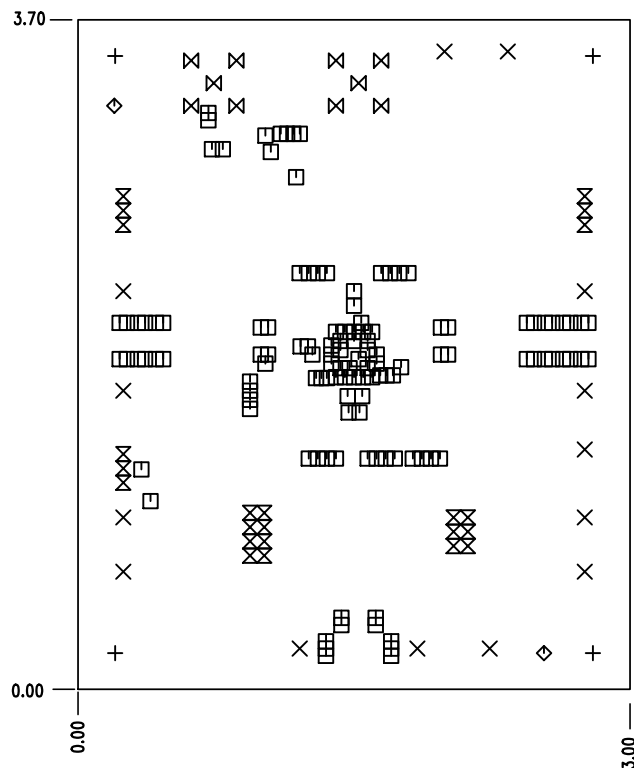
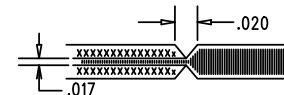


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	SY	9-03-15


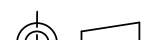


## NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062 +/- .005  
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- PCBS ARE TO BE RoHS COMPLIANT.
- DESIGN HAS SOLDER MASK DEFINED PADS ON U1.  
U1 SOLDER MASK SIZE IS 25 MIL AND PAD SIZE IS 29 MIL.  
DON'T CHANGE PAD SIZE OR SOLDER MASK SIZE.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



SIZE	QTY	SYM	PLATED	TOL
0.01	125	□	YES	+/-0.003
0.035	23	⊗	YES	+/-0.003
0.055	10	⊗	YES	+/-0.003
0.07	2	◇	NO	+/-0.003
0.095	14	⊗	YES	+/-0.003
0.187	4	+	YES	+/-0.005

UNLESS OTHERWISE SPECIFIED		APPROVALS		 <b>LINEAR TECHNOLOGY</b>		1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 <a href="http://www.linear.com">www.linear.com</a> LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX = ± 0.01 0.XXX = ± 0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION							
		PCB DES.	LT	TITLE: FABRICATION DRAWING			
		APP ENG.	SY				
				15V DUAL 3A DDR $\mu$ MODULE REGULATOR			
				SIZE		IC NO.	REV
				N/A		LTM4632EV DEMO CIRCUIT 2367A	
		SCALE = NONE		FILENAME: DC2367A-2.PCB			SHT 1 OF 1