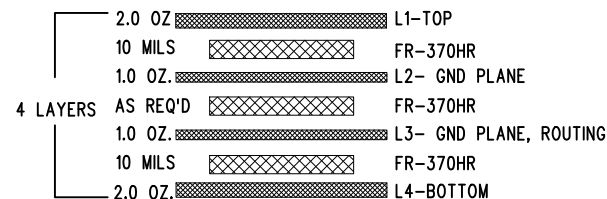


NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS. FINISHED COPPER PLATING SHALL BE 2 OZ. OUTER LAYERS, 1 OZ. INNER LAYERS. STACKUP AS PER FIG.1, "STACKUP DETAILS".
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATED-THROUGH HOLE COPPER THICKNESS SHALL BE 1 OZ. MINIMUM.
- HOLE LOCATION TOLERANCES SHALL BE 5 MILS DIAMETER TRUE POSITION.
- ALL 12.5 MIL DIA. VIAS, QTY. 13, SHALL BE IPC 4761 CLASS VII FILLED AND CAPPED VIAS.
- ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-SILKSCREEN LEGENDS SHALL BE WHITE NON-CONDUCTIVE EPOXY INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. BOARDS SHALL BE RoHS COMPLIANT.
8. BOARDS SHALL BE PANELIZED USING ROUTE AND RETAIN METHOD. THERE SHALL BE ONE RETAINING TAB ON EACH BOARD EDGE.
9. ALL ELECTRICAL TEST STAMPS SHALL BE ON BOTTOM SIDE.

FIG. 1
STACKUP DETAILS



SIZE	QTY	SYM	PLATED	TOL
188	4	+	NO	+/- 3 MILS
13	103	X	YES	+/- 3 MILS
65	9	□	YES	+/- 6 MILS
95	8	◇	YES	+/- 3 MILS
70	2	⊗	NO	+/-0.003
12	86	+ ^A	YES	+/- 3 MILS
35	37	+ ^B	YES	+/- 3 MILS
210	7	+ ^C	YES	+/-0.003
40	12	+ ^D	YES	+/-0.003
12.5	13	+ ^E	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION	APPROVALS		1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
	PCB DES.	M. HAWKINS		
	APP ENG.	V. OSTREROV	TITLE: FABRICATION DRAWING	
	IC NO. LTC4236-X		DUAL IDEAL DIODE-OR AND SINGLE HOT SWAP CONTROLLER WITH CURRENT MONITOR	
		SKU NO. DC2314A-X		DOC. NUMBER: 615-DC2314A
				REV: 01
		SIZE: N/A	SCALE=NONE	SHT 1 OF 1