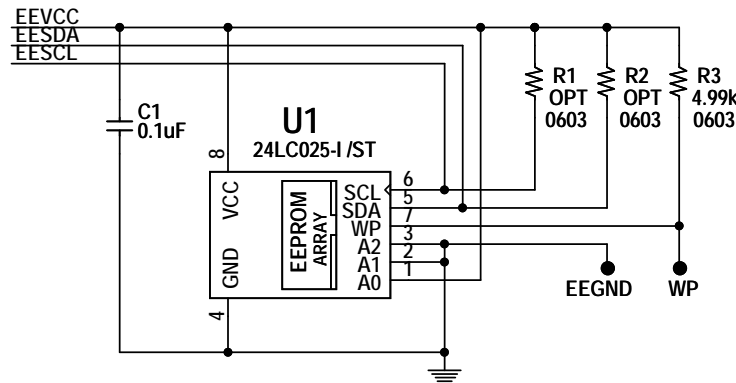
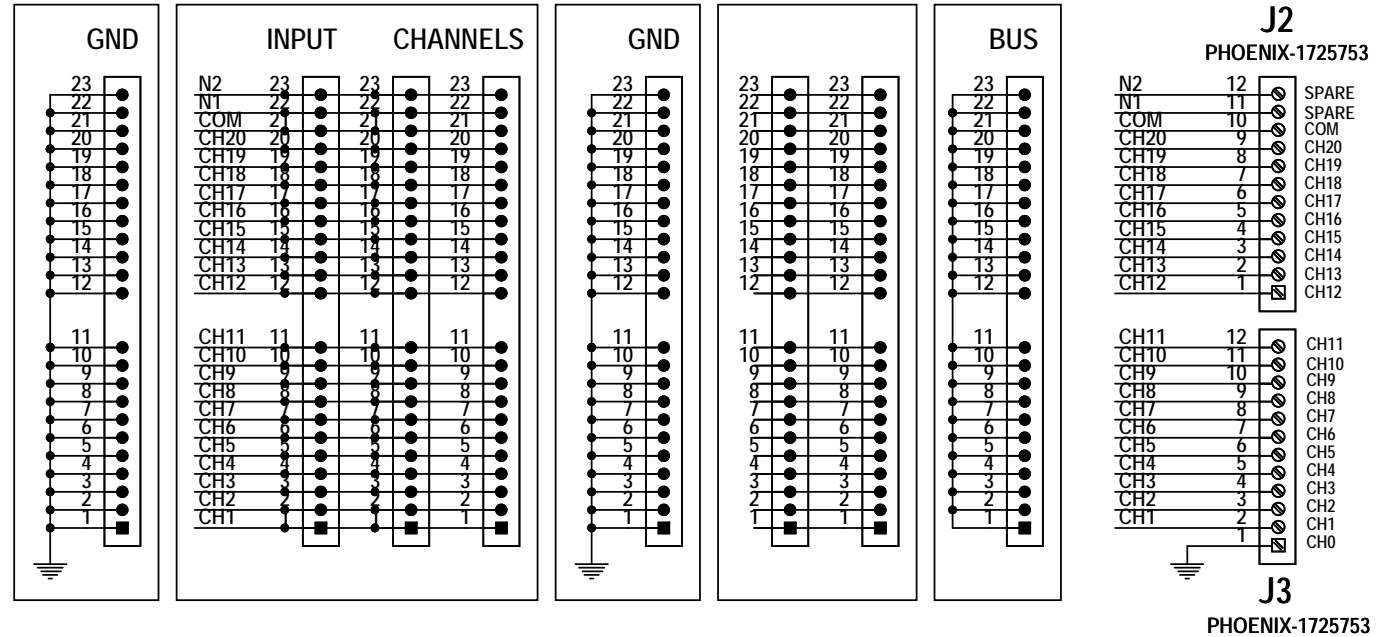
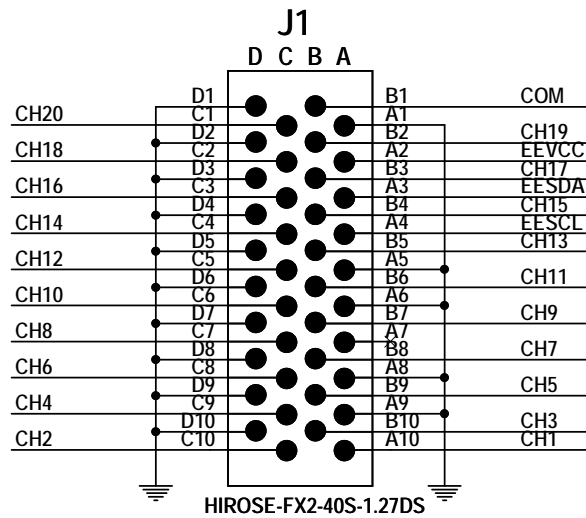


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	1	1ST PROTOTYPE	MARK T.	05-21-14



NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL CAPACITORS ARE IN MICROFARADS, 0603.
2. ALL RESISTORS ARE IN OHMS, THROUGH HOLE.

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES. KIM T.

APP ENG. MARK T.

SCALE = NONE



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Fax: (408)434-0507
LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC

EXPERIMENTER BOARD

SIZE
N/A

IC NO.

DEMO CIRCUIT 2210A

REV.
1

DATE: 05/21/2014, 06:25 PM

SHEET 1 OF 1