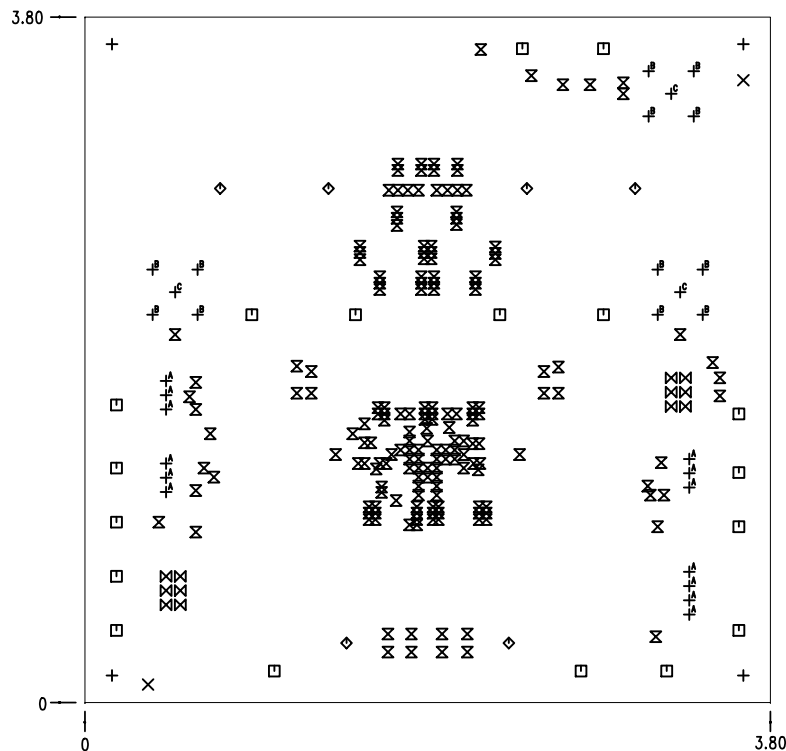


REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG. DATE
-	2	PRODUCTION	GL 02-09-15



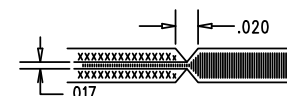
SIZE	QTY	SYM	PLATED	TOL
0.01	182	⊗	YES	+/-0.003
0.031	13	+ <sup>A</sup>	YES	+/-0.003
0.035	12	⊗	YES	+/-0.003
0.055	3	+ <sup>C</sup>	YES	+/-0.003
0.059	12	+ <sup>B</sup>	YES	+/-0.003
0.07	2	⊗	NO	+/-0.003
0.095	18	□	YES	+/-0.003
0.187	4	+	NO	+/-0.005
0.215	6	◇	YES	+/-0.005

## LAYER STRUCTURE


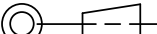
—	LAYER 1	COMPONENT SIDE
—	LAYER 2	GND
—	LAYER 3	SIG/GND
—	LAYER 4	SOLDER SIDE

## NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4  
-FINISHED THICKNESS TO BE 0.062 +/- .005  
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0/0 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)  
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



- DESIGN HAS SOLDER MASK DEFINED PADS ON U1 WITH 0.029 PAD, 0.025 SOLDER MASK, AND 0.024 SOLDER PASTE.  
DO NOT CHANGE ANY SIZE ON THIS COMPONENT.

UNLESS OTHERWISE SPECIFIED		APPROVALS		 <b>LINEAR TECHNOLOGY</b>		1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX = ± 0.01 0.XXX = ± 0.005  INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		PCB DES.	LT				
		APP ENG.	GL	TITLE: FABRICATION DRAWING  HIGH DENSITY, DUAL 4A  STEP-DOWN $\mu$ MODULE REGULATOR			
				SIZE	IC NO.	LTM4642EY	REV
				N/A		DEMO CIRCUIT 2194A	2
SCALE = NONE		FILENAME: DC2194A-2.PCB				SHT 1 OF 1	