

REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	C.MAYOTT	03-18-14

## NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL:
  - PCB'S SHALL BE RoHS COMPLIANT.
  - BASE MATERIAL SHALL BE FR408HR OR EQUIVALENT.
  - FINISHED THICKNESS SHALL BE 0.062"  $\pm 10\%$ .
  - TOTAL OF 6 LAYERS, FINISHED COPPER THICKNESS AS PER FIG.1, "LAYER STRUCTURE".
  - FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
- DRILLING:
  - DRILL HOLES PER SCHEDULE, HOLE PLATING 1 MIL THICK MINIMUM.
  - ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
  - HOLE LOCATION TOLERANCES AS FOLLOWS:
    - 3 MILS DIAMETER TRUE POSITION TOLERANCE FOR ALL HOLES 40 MILS DIAMETER OR LESS.
    - 6 MILS DIAMETER TRUE POSITION TOLERANCE FOR ALL OTHER HOLES.
  - NOTE BLIND VIAS, 6 MIL DIA., QTY. 228, LAYER 1 TO LAYER 2, PLATED SHUT.
- FINISH:
  - SMOBC USING LPI BOTH SIDES, COLOR BLACK.
  - ENIG FINISH BOTH SIDES IN ACCORDANCE WITH IPC-4552.
  - SILKSCREEN LEGENDS SHALL BE WHITE NON-CONDUCTIVE EPOXY INK.
- FABRICATOR SHALL NOT ADD LOGO OR DATE CODE TO ARTWORK.
- BOARDS SHALL BE PANELIZED USING ROUTE AND RETAIN METHOD. THERE SHALL BE A MINIMUM OF TWO RETAINING TABS ON ALL BOARD EDGES.
- BOARD USES THREE DIFFERENT FIXED IMPEDANCE DESIGN GEOMETRIES AS FOLLOWS:
  - SURFACE MICROSTRIP PRIMARY SIDE, 30 MILS WIDE, IMPEDANCE 50 OHMS  $\pm 10\%$  AT 2Ghz, REFERENCE PLANE LAYER 3.
  - BURIED MICROSTRIP LAYER 2, 15 MILS WIDE, IMPEDANCE 50 OHMS  $\pm 10\%$  AT 2Ghz, REFERENCE PLANE LAYER 3.
  - SURFACE MICROSTRIP SECONDARY SIDE, 30 MILS WIDE, IMPEDANCE 50 OHMS  $\pm 10\%$  AT 2Ghz, REFERENCE PLANE LAYER 4.
- VIA FILLING
  - ALL 12.0 MIL DIAMETER VIAS, QTY 38, SHALL BE IPC 4761 TYPE VII FILLED AND CAPPED VIAS.
- ELECTRICAL TEST STAMPS SHALL BE ON SECONDARY SIDE ONLY.

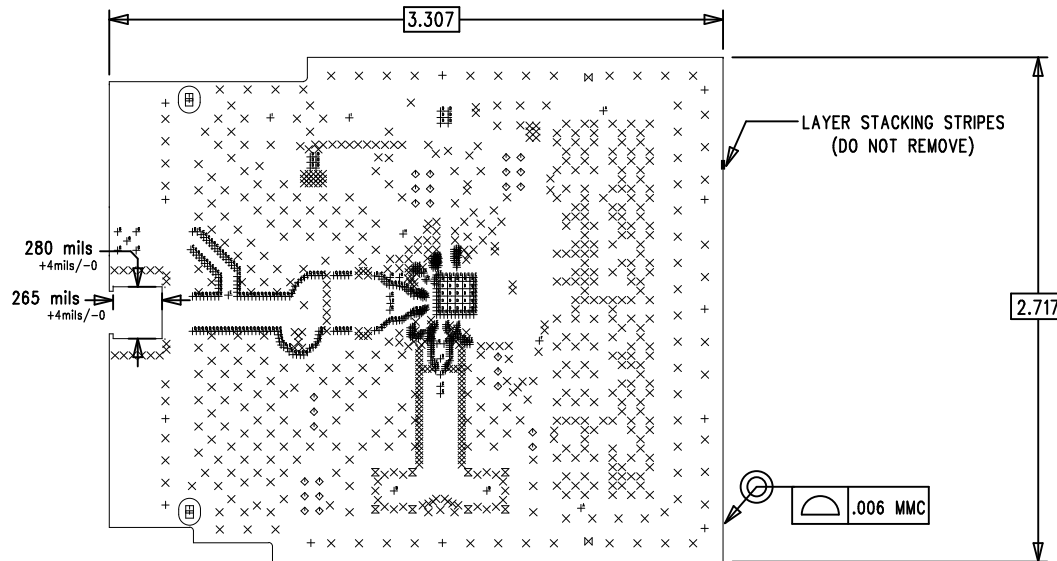

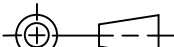


FIG.1  
LAYER STRUCTURE

SIZE	QTY	SYM	PLATED	THR/PRTL	TOL
86.61	12	+	YES	THR	$\pm 3$ MILS
10	699	X	YES	THR	$\pm 3$ MILS
118.11 x 145.67	2	□	NO	THR	$\pm 3$ MILS
35	26	◇	YES	THR	$\pm 3$ MILS
67	8	⊗	YES	THR	$\pm 3$ MILS
135	2	⊗	YES	THR	$\pm 3$ MILS
50	2	⊕	NO	THR	$\pm 0.003/-0.000$
59	2	⊕	YES	THR	$\pm 3$ MILS
8	156	⊕	YES	THR	$\pm 3$ MILS
12	38	⊕	YES	THR	$\pm 3$ MILS
6	228	⊕	YES	PI-2	$\pm 3$ MILS
20	1	⊕	YES	THR	$\pm 3$ MILS
32	5	⊕	YES	THR	$\pm 3$ MILS
65	1	⊕	YES	THR	$\pm 3$ MILS
95	2	⊕	YES	THR	$\pm 3$ MILS

1.5 OZ.		LAYER 1- PRIMARY SIDE
4.5 MILS		FR408HR
2.0 OZ.		LAYER 2- GND
10 MILS		FR408HR
1 OZ.		LAYER 3- PWR PLANE/GND
THICKNESS AS REQ'D		FR408HR
1 OZ.		LAYER 4- PWR PLANE/GND
10 MILS		FR408HR
2.0 OZ.		LAYER 5- GND
4.5 MILS		FR408HR
1.5 OZ.		LAYER 6- SECONDARY SIDE

UNLESS OTHERWISE SPECIFIED		APPROVALS		 <div>1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY</div>		
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ± 0.01" 0.XXX" = ± 0.005"  INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		PCB DES.	M.HAWKINS			
		APP ENG.	C.MAYOTT	TITLE: FABRICATION DRAWING 16-BIT, 210 Msp/s ADC		
				SIZE N/A	IC NO. LTC2107 DEMO CIRCUIT 2183A	REV 2
		SCALE = NONE		FILENAME: DC2183A-2.PCB		
				SHT 1 OF 1		