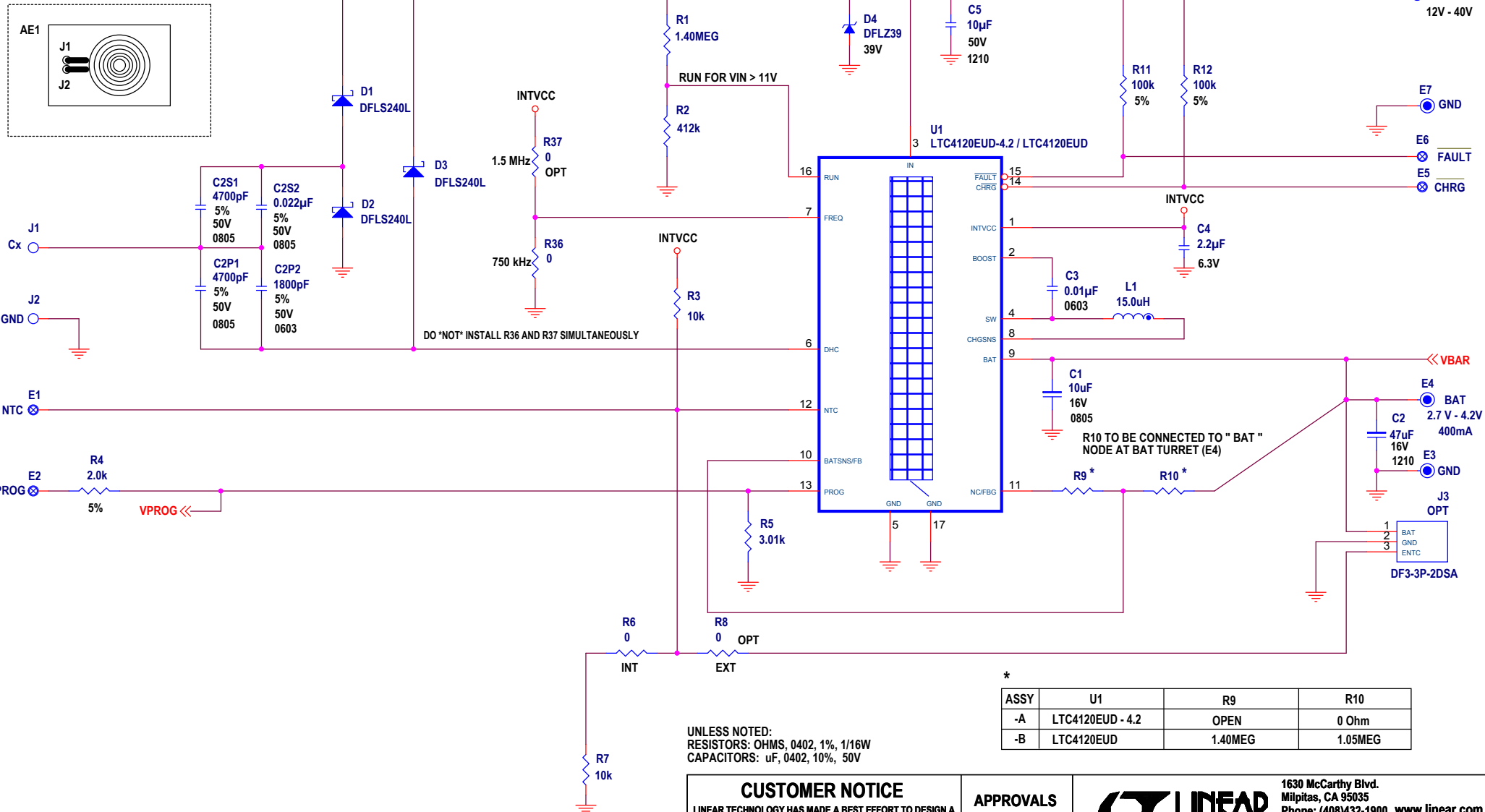


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	3	PRODUCTION	GEORGE B.	7 - 17 - 14

FIGURE 1.
NOTE: PCB AE1 CONNECTS TO J1 AND J2



UNLESS NOTED:
RESISTORS: OHMS, 0402, 1%, 1/16W
CAPACITORS: uF, 0402, 10%, 50V

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES. NC
APP ENG. GEORGE B.

SCALE = NONE



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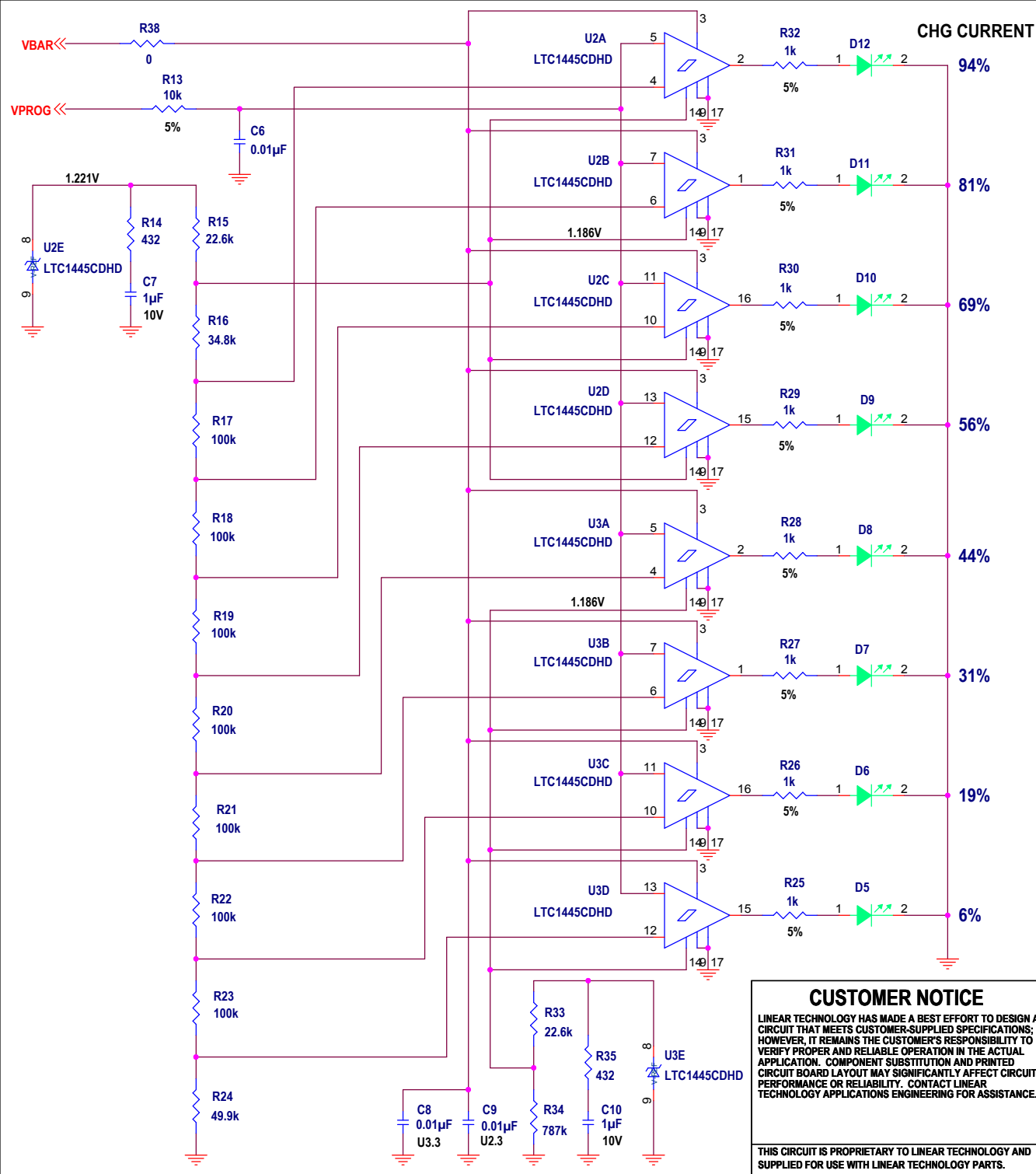
TITLE: SCHEMATIC

400mA WIRELESS SYNCHRONOUS BUCK BATTERY CHARGER


SIZE N/A IC NO. LTC4120EUD - 4.2 / LTC4120EUD
DEMO CIRCUIT 2181A - A / B REV. 3

DATE: 7 - 17 - 14

SHEET 1 OF 2



UNLESS NOTED:
RESISTORS: OHMS, 0402, 1%, 1/16W
CAPACITORS: uF, 0402, 10%, 50V

APPROVALS		 <div> 1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 www.linear.com Fax: (408)434-0507 LTC Confidential-For Customer Use Only </div>		
PCB DES.	NC			
APP ENG.	GEORGE B.	TITLE: SCHEMATIC BAR GRAPH FOR 400mA WIRELESS SYNCHRONOUS BUCK BATTERY CHARGER		
		SIZE N/A	IC NO. LTC4120EUD - 4.2 / LTC4120EUD DEMO CIRCUIT 2181A - A / B	REV. 3
SCALE = NONE		DATE: 7 - 17 -14		
		SHEET 2 OF 2		