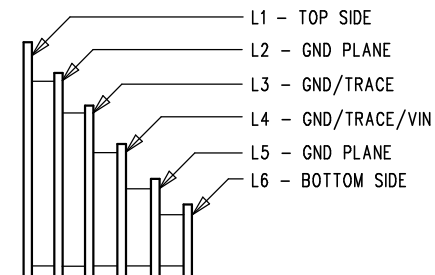


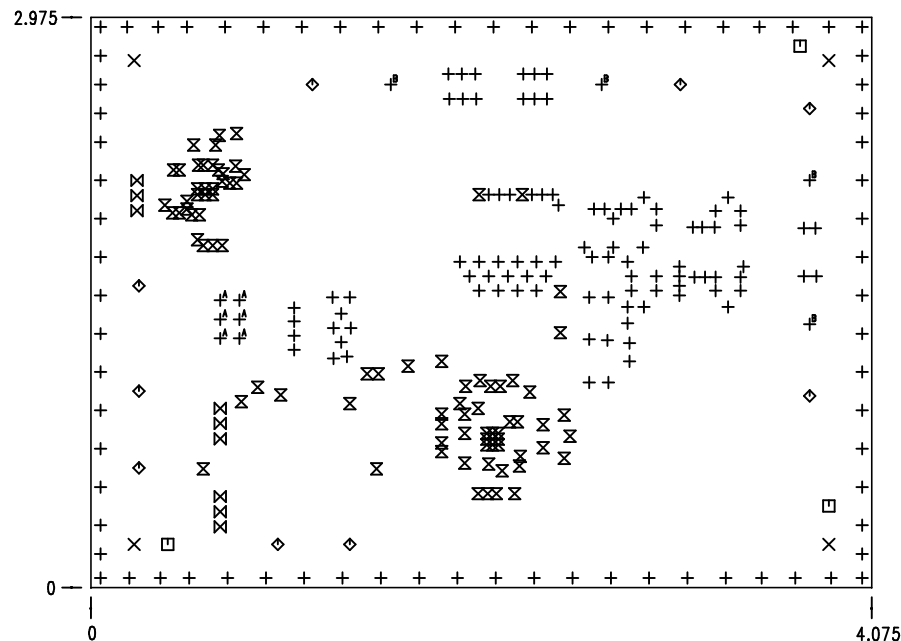
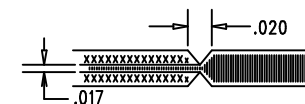
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	2	PRODUCTION	VICTOR K	05-02-14

## LAYER STRUCTURE

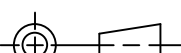



## NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062+/- .005  
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
O/O ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



SIZE	QTY	SYM	PLATED	TOL
0.01	86	⊗	YES	+/-0.003
0.02	172	+	YES	+/-0.003
0.031	9	⊗	YES	+/-0.003
0.035	6	⊕ <sup>A</sup>	YES	+/-0.003
0.07	3	□	NO	+/-0.003
0.094	9	◇	YES	+/-0.003
0.187	4	⊗	NO	+/-0.003
0.205	4	⊕ <sup>B</sup>	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED  DIMENSIONS ARE IN INCHES TOLERANCES:  0.XX = ± 0.01 0.XXX = ± 0.005  INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION  	APPROVALS		 <b>LINEAR</b> TECHNOLOGY	1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY			
	PCB DES.	LT					
	APP ENG.	VICTOR K.	TITLE: FABRICATION DRAWING  HIGH VOLTAGE, LOW IQ, SYNCHRONOUS BOOST CONVERTER				
			SIZE		IC NO.	LTC3769EUF	REV
			N/A		DEMO CIRCUIT 2173A		2
			SCALE = NONE		FILENAME: DC2173A-2.PCB		SHT 1 OF 1