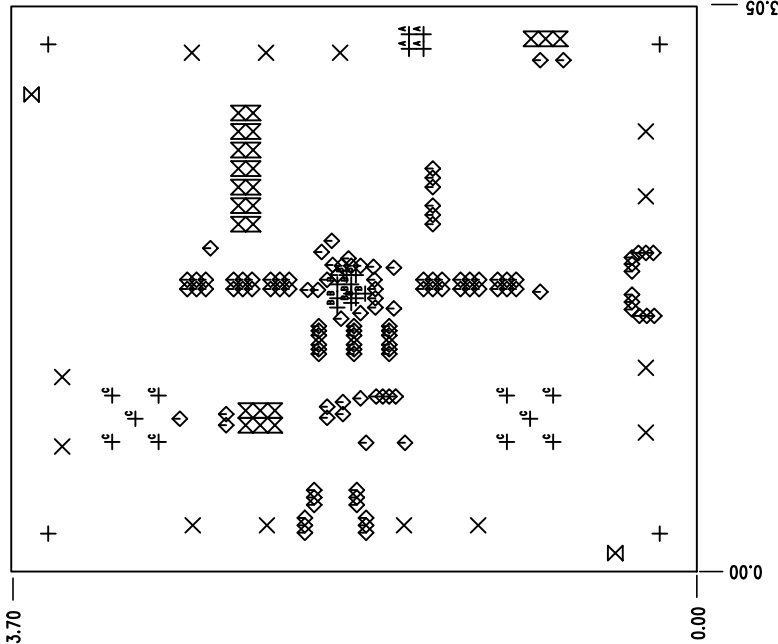
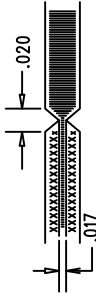


REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG. DATE
-	1	PRODUCTION	SAM Y. 10-30-13



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
5. FINISH: -SNOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. DESIGN HAS SOLDER MASK DEFINED PADS ON U1.
U1 SOLDER MASK SIZE IS 25 MIL AND PAD SIZE IS 29 MIL.
DON'T CHANGE PAD SIZE OR SOLDER MASK SIZE.
9. SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



UNLESS OTHERWISE SPECIFIED

DIMENSIONS ARE IN INCHES

TOLERANCES:

0.XX = ± 0.01

0.XXX = ± 0.005

INTERPRET DIM AND TOL PER ASME Y14.5M-1994

THIRD ANGLE PROJECTION

APPROVALS

PCB DES.	Hz
APP ENG.	SAM Y.
SCALE = NONE	

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TITLE: FABRICATION DRAWING

STEP-DOWN μMODULE® REGULATOR

SIZE	IC NO.	LTW4623EY/LTM4625EY	REV
N/A		DEMO CIRCUIT 2171A	1

FILENAME: DC2171A-I.PCB

SHT 1 OF 1