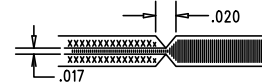


REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG. DATE
-	1	PROTOTYPE	NICK V. 01-29-14

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 2 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN. 0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):

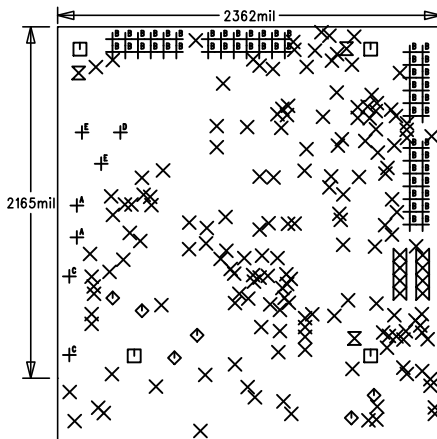


STACK UP DETAILS

L1	Prepeg 0.007 +/- 0.002	2 oz
L2	Adjust	2 oz
L3	Prepeg 0.007 +/- 0.002	2 oz
L4	Prepeg 0.007 +/- 0.002	2 oz

Board Thickness = 0.062 +/- 0.005

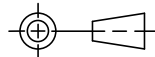
SHOWN FROM TOP SIDE



DRILL DRAWING LAYER
LINEAR TECHNOLOGY DATE: 01-29-14
DC2086A-1 + LTM4676YE
Digital Power Programming Adapter

SIZE	QTY	SYM	PLATED	TOL
15	164	X	YES	+/-0.003
150	4	□	YES	+/-0.003
28	6	◇	YES	+/-0.003
70	3	⊗	NO	+/-0.003
40	8	⊗	YES	+/-0.003
55.12	2	⊕ ^A	YES	+/-0.003
35	52	⊕ ^B	YES	+/-0.003
49.21	2	⊕ ^C	YES	+/-0.003
137.8	1	⊕ ^D	YES	+/-0.003
118.11	2	⊕ ^E	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION



APPROVALS

PCB DES.	S.M.
APP ENG.	NICK V.
SCALE = NONE	



TITLE: FABRICATION DRAWING
Digital Power Programming Adapter

SIZE	IC NO.	REV
N/A	LTM4676YE DEMO CIRCUIT 2086A	1
FILENAME: DC2086A-1.PCB	SHT 1 OF 1	