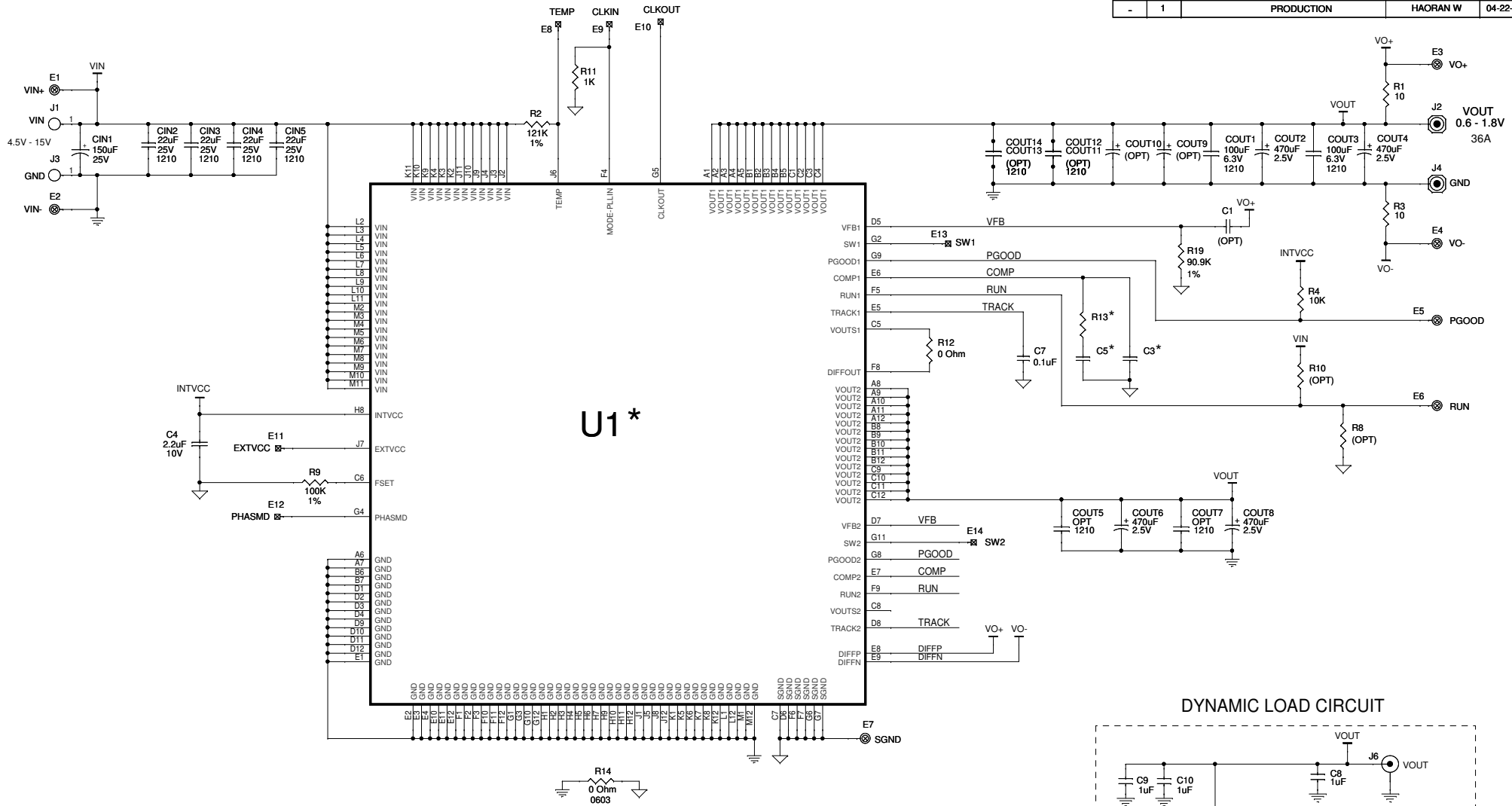
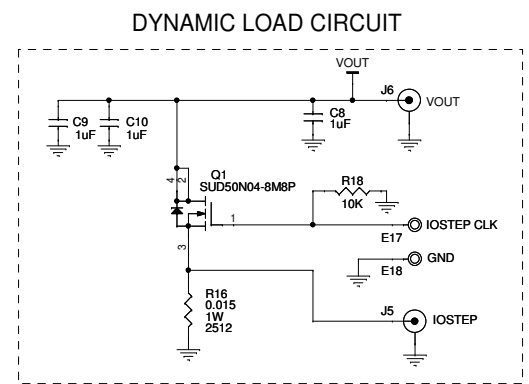


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	1	PRODUCTION	HAORAN W	04-22-14



ASSY	U1	C3	C5	R13
-A	LTM4630EV	OPT	OPT	OPT
-B	LTM4630EY-1	47pF	1.5nF	7.15K



NOTE: UNLESS OTHERWISE SPECIFIED
 1. ALL RESISTORS ARE IN OHMS, 0603.
 ALL CAPACITORS ARE IN MICROFARADS, 0603.

CUSTOMER NOTICE LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		APPROVALS PCB DES. LT APP ENG. HAORAN W		 1630 McCarthy Blvd. Milpitas, CA 95035 www.linear.com Phone: (408)432-1900 Fax: (408)434-0507 LTC CONFIDENTIAL - FOR CUSTOMER USE ONLY	
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		SCALE = NONE		TITLE: SCHEMATIC HIGH EFFICIENCY, DUAL PHASE, SINGLE OUTPUT STEP-DOWN POWER μMODULE REGULATOR SIZE N/A IC NO. LTM4630EV / LTM4630EY-1 DEMO CIRCUIT DC2081A DATE: Tuesday, April 22, 2014	
				REV. 1 SHEET 1 OF 1	