


TOP: SILKSCREEN LAYER
LINEAR TECHNOLOGY
DC2063A-2 * LTC2875
60V FAULT PROTECTED 25KV ESD CAN TRANSCEIVER
DATE: 6-24-14

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	2	2ND PROTOTYPE	KEITH B.	6-24-14

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
 2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER SMD SIDES. MAXIMUM SOLDER TEMPERATURE IS 250 DEGREES CELSIUS.
 3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. CLOSE-OFF THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
 4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
 5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
 6. ASSEMBLY STAMP OR QA STAMP BOTTOM SIDE ONLY.
7. INSTALL 4 STANDOFFS AT 4 LOCATIONS AS SHOWN.

APPROVALS		 LINEAR TECHNOLOGY		1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408) 432-1900 Fax: (408) 434-0507 www.linear.com		LTC CONFIDENTIAL FOR CUSTOMER USE ONLY
PCB DES.	KWB					
APP ENG.	KWB					
		TITLE: TOP ASSEMBLY DRAWING 60V FAULT PROTECTED 25KV ESD CAN TRANSCEIVER				
		SIZE	IC NO.			REV.
		N/A	LTC2875HDD DEMO CIRCUIT 2063A			2
SCALE = NONE		DATE: Tuesday June 24, 2014			SHEET 1 OF 1	