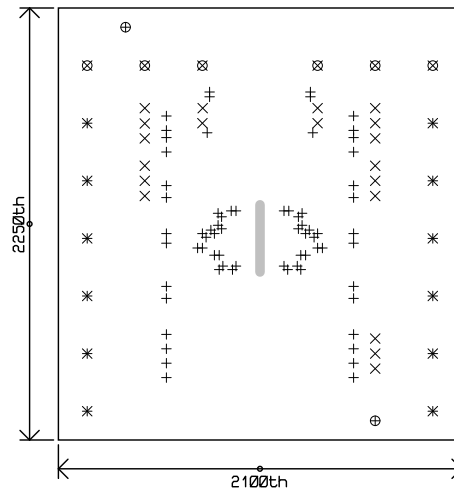


SIZE	QTY	SYM	PLATED	TOL
0.010"	70	+	YES	+/-0.003"
0.030"	19	×	YES	+/-0.003"
0.066"	12	*	YES	+/-0.003"
0.070"	2	⊕	NO	+/-0.003"
0.096"	6	⊗	YES	+/-0.003"

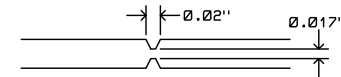
SHOWN FROM COMPONENT SIDE



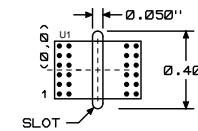
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	1	1ST PROTOTYPE	KEITH B.	4-17-12

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 2 LAYERS WITH 1 OZ. CU ON EACH LAYER
-FLAMMABILITY RATING: 94v-0 MINIMUM
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING
-HOLE LOCATION TOLERANCES ARE +/- 0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN
-GOLD IMMERSION BOTH SIDES
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE MAY BE MODIFIED TO MEET END FINISH.
- PCBs ARE TO BE RoHS COMPLIANT
- DESIGN HAS SOLDER MASK DEFINED PADS ON U1.
U1 SOLDER MASK SIZE IS 20 MIL AND PAD SIZE IS 24 MIL.
DO NOT CHANGE ANY SIZE ON THIS COMPONENT.
- SCORING FOR PANELIZED PCB:


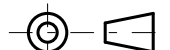


10. SLOT DIMENSION:



LAYER STRUCTURE



UNLESS OTHERWISE SPECIFIED		APPROVALS		 <div>1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408) 432-1900 Fax: (408) 434-0507 www.linear.com</div>		LTC CONFIDENTIAL FOR CUSTOMER USE ONLY			
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = +/- 0.01" 0.XXX" = +/- 0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994		PCB DES. KEITH B.							
THIRD ANGLE PROJECTION		APP ENG. KEITH B.		TITLE: FABRICATION DRAWING I2C uMODULE ISOLATOR					
				SIZE		IC NO.		REV.	
				N/A		LTM2892CY-I DEMO CIRCUIT 1986A		1	
SCALE = 1:1		DATE: Tuesday April 17, 2012				SHEET 1 OF 1			