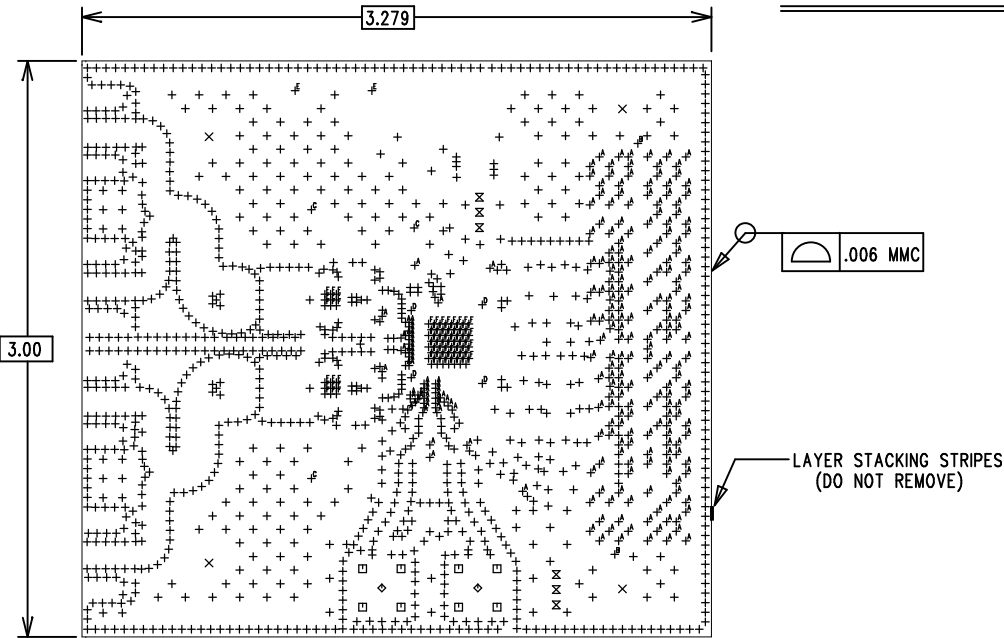


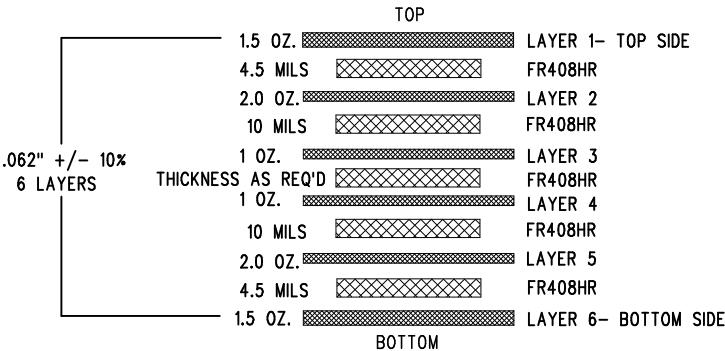
NOTES: UNLESS OTHERWISE SPECIFIED

REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	3	PRODUCTION	C.MAYOTT	10-18-13



1. FAB PER IPC-A-600.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR408HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 6 LAYERS, FINISHED COPPER THICKNESS AS PER FIG.1, "LAYER STRUCTURE"
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES SHALL BE 6 MILS DIAMETER TRUE POSITION.
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR BLACK.
-GOLD IMMERSION BOTH SIDES.
-SILKSCREEN LEGENDS SHALL BE WHITE NON-CONDUCTIVE EPOXY INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. BOARDS SHALL BE PANELIZED USING ROUTE AND RETAIN METHOD.
9. BOARD USES THREE DIFFERENT FIXED IMPEDANCE DESIGN TECHNIQUES AS FOLLOWS:
 - 9.1 ALL 30 MIL WIDE TRACES ON TOP SIDE, ARE DESIGNED AS SURFACE MICROSTRIP, AND SHALL HAVE AN IMPEDANCE OF 50 OHMS +/- 10%, MEASURED FROM LAYER 1 TO THE LAYER 3 GROUND PLANE LAYER, AT A FREQUENCY OF 2 GHZ.
 - 9.2 DIFFERENTIAL PAIRS ARE ON TOP SIDE. TRACES ARE 6 MILS WIDE WITH 6 MIL GAP, AND SHALL HAVE A DIFFERENTIAL IMPEDANCE OF 100 OHMS +/- 10%, MEASURED FROM LAYER 1 TO THE LAYER 2 GROUND PLANE LAYER, AT A FREQUENCY OF 2 GHZ.
 - 9.3 ALL 8 MIL WIDE TRACES ON TOP SIDE ARE DESIGNED AS SURFACE MICROSTRIP, AND SHALL HAVE AN IMPEDANCE OF 50 OHMS +/- 10%, MEASURED FROM LAYER 1 TO THE LAYER 2 GROUND PLANE LAYER, AT A FREQUENCY OF 2 GHZ.
 - 9.4 STACKUP AS PER FIG.1, "LAYER STRUCTURE".
10. ALL 12.0 MIL DIAMETER VIAS, QTY 78, SHALL BE IPC 4761 TYPE IIIa SINGLE-SIDED SOLDERMASK-PLUGGED VIAS, WITH VIA PLUG ON PRIMARY SIDE.

FIG.1
LAYER STRUCTURE



SIZE	QTY	SYM	PLATED	TOL
10	1106	+	YES	+/- 3 MILS
125	4	X	YES	+/- 3 MILS
67	8	□	YES	+/- 3 MILS
59	2	◇	YES	+/- 3 MILS
35	6	⊗	YES	+/- 3 MILS
8	243	+ ^A	YES	+/- 3 MILS
50	2	+ ^B	NO	+0.003/-0.000
65	3	+ ^C	YES	+/- 3 MILS
15	4	+ ^D	YES	+/- 3 MILS
95	2	+ ^E	YES	+/- 3 MILS
12	78	+ ^F	YES	+/- 3 MILS

DRILL CHART

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION

APPROVALS

PCB DES.	M.HAWKINS
APP ENG.	CLARENCE M.
SCALE = NONE	

LINEAR TECHNOLOGY
1630 MCCARTHY BLVD
MILPITAS, CA 95035
PH: (408)432-1900
www.linear.com
LTC CONFIDENTIAL -
FOR CUSTOMER USE ONLY

TITLE: FABRICATION DRAWING
LTC2158 AND LTC6430
COMBO BOARD

SIZE	IC NO. LTC2158CUP-14, LTC6430AIUF-15	REV
N/A	DEMO CIRCUIT 1946A	3

FILENAME: DC1946A-3.PCB SHT 1 OF 1