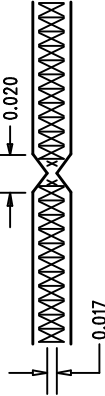


FAB Drawing

NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600. PCBs ARE TO BE ROHS COMPLIANT.
2. MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4.
2 OZ. COPPER FINISH ON OUTER LAYERS.
1 OZ. COPPER FINISH ON INNER LAYERS.
THICKNESS .062 +/- .006 TOTAL OF 4 LAYERS.
FLAMMABILITY RATING: 94 V-0 MINIMUM .
3. SIZE: DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, .001 INCH THICK MIN. ALL
HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/- .003
INCH IN RELATION TO CENTER
5. FINISH: SMOBC USING LPI BOTH SIDES COLOR GREEN.
GOLD IMMERSION BOTH SIDES.
6. SILKSCREEN : USING WHITE NON-CONDUCTIVE EPOXY INK.
7. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE
BUT YOU MAY MODIFY PAD SIZE TO MEET END FINISH.
8. SCORING: (FOR PRODUCTION RUN ONLY)



9. DESIGN HAS SOLDER MASK DEFINED_PADS ON U1. U1 SOLDER MASK
SIZE IS 25 MIL AND PAD SIZE IS 29 MIL. DO NOT CHANGE ANY
ANY SIZE ON THIS COMPONENT.

Fabrication Drawing

APPROVALS		LINEAR TECHNOLOGY	
DRAWN	INT	DATE	
CHECK			
DESIGN	J. Wu	3/28/08	
ENGR	H. Moo	3/28/08	
		TITLE: LTM4601AEV/LTM4601AHVEV	
		High Efficiency DC/DC μ Module	
		Demo Circuit 1414B	REV. 3
SCALE = NONE		SHT 1 of 1	

1630 McCarthy Blvd.
Milpitas, CA 95035
PH: (408)432-1900

SIZE	QTY	SYM	PLATED	TOL
70	2	+	NO	+/-0.003
187	4	X	YES	+/-0.003
63	13	□	YES	+/-0.003
35	14	◇	YES	+/-0.003
10	174	⊗	YES	+/-0.003
205	4	⊗	YES	+/-0.003
31	9	⊕ ^A	YES	+/-0.003
52	8	⊕ ^B	YES	+/-0.003
59	2	⊕ ^C	YES	+/-0.003