

REVISIONS			
REV	DESCRIPTION	APPR	DATE

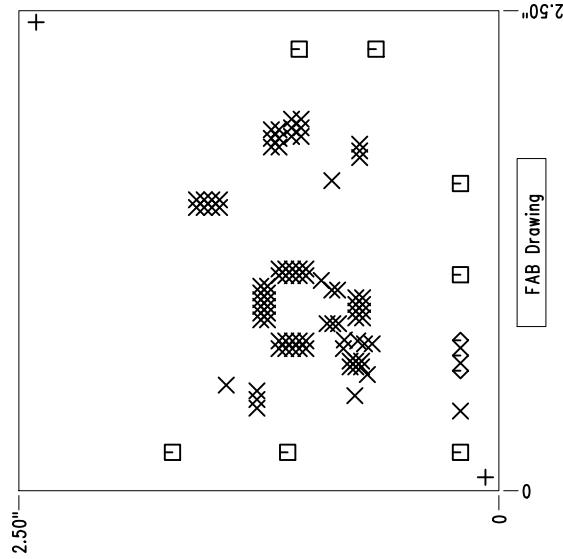
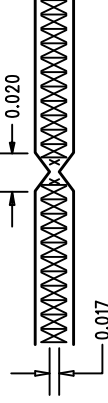
NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600. PCBs ARE TO BE ROHS COMPLIANT.
2. MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4.  
2 OZ. COPPER FINISH ON OUTER LAYERS.  
1 OZ. COPPER FINISH ON INNER LAYERS.  
THICKNESS .062 +/- .006 TOTAL OF 4 LAYERS.  
FLAMMABILITY RATING: 94 V-0 MINIMUM .
3. SIZE: DIMENSIONS AND TOLERANCES SHOWN.  
0.00 ARE PRIMARY DATUMS.
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH  
HOLES WITH COPPER, .001 INCH THICK MIN. ALL  
HOLE SIZES ARE SPECIFIED AFTER PLATING.  
HOLE LOCATION TOLERANCES ARE +/- .003  
INCH IN RELATION TO CENTER
5. FINISH: SMOBC USING LPI BOTH SIDES COLOR GREEN.

**GOLD IMMERSION BOTH SIDES.(LEAD FREE SOLDER FOR PROTOTYPE)**

6. SILKSCREEN : USING WHITE NON-CONDUCTIVE EPOXY INK.  
7. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
BUT YOU MAY MODIFY PAD SIZE TO MEET END FINISH.

8. SCORING: (FOR PRODUCTION RUN ONLY)



SIZE	QTY	SYM	PLATED	TOL
0.07	2	+	NO	+/-0.003
0.01	88	X	YES	+/-0.003
0.095	7	□	YES	+/-0.003
0.035	3	◇	YES	+/-0.003

# FAB DRAWING

UNLESS OTHERWISE SPECIFIED  
DIMENSIONS ARE IN INCHES  
TOLERANCES ON ANGLE  $\pm 1$   
2 PLACE  $\pm .01$  3 PLACE  $\pm .005$   
INTERPRET DIM AND TOL  
PER ASME Y14.5M - 1994



**LINEAR**  
**TECHNOLOGY**

**TITLE:** LT3757AEDD

## High Efficiency SEPIC Converter

SIZE	A	DEMO	DC1341B	REV. 1
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SHT 1 of 1