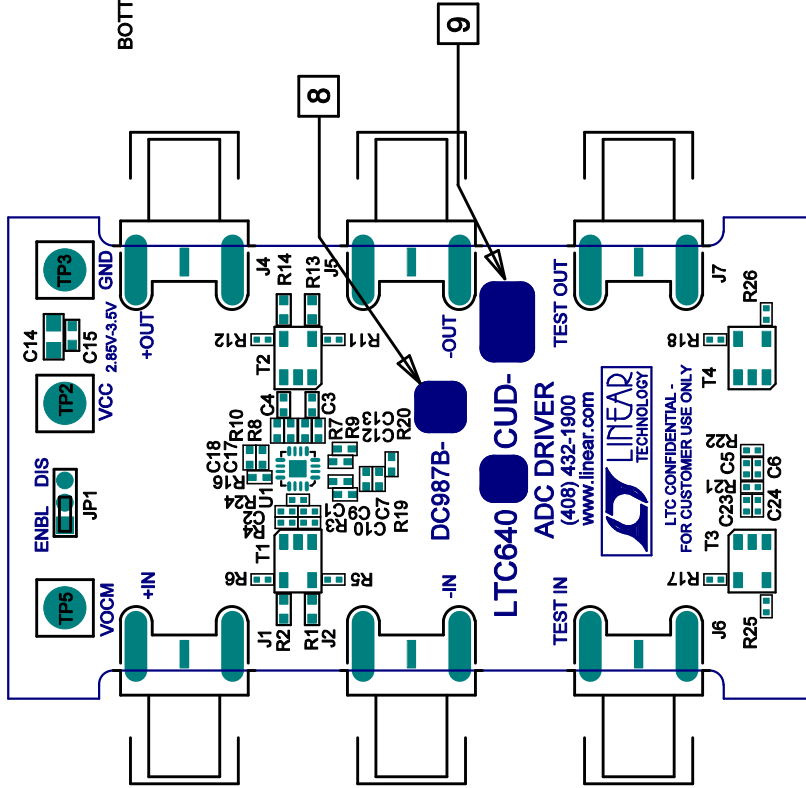


## INSTALL SHUNT; JP1 - PINS 1&2



## NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. INSERT SMA CONNECTORS FLUSH AGAINST BOARD EDGE.
3. ASSY PROCESSES SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.  
REFLOW BOTTOM SIDE SMD.
4. PARTS TO OMIT WILL BE SPECIFIED ON THE PARTS LIST.  
LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER.  
MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
5. SHORTAGES SHALL EXIST ON THE MINIMUM NUMBER OF BOARDS.
6. DO NOT OTHERWISE MARK BOARDS (e.g. NO QA-STAMP).
7. DEPANELIZE BOARDS AFTER ASSEMBLY. DO NOT BEND.  
USE DIAGONAL CUTTERS TO SEPARATE PCB'S.
- 8 MARK DASHED LETTER VERSION APPROX. WHERE SHOWN.
- 9 MARK APPROPRIATE IC, WHERE SHOWN.

VERSION	IC
-A	LTC6400CUD-8
-B	LTC6400CUD-14
-C	LTC6400CUD-20
-D	LTC6400CUD-26
-E	LTC6401CUD-8
-F	LTC6401CUD-14
-G	LTC6401CUD-20
-H	LTC6401CUD-26

TOP SILKSCREEN