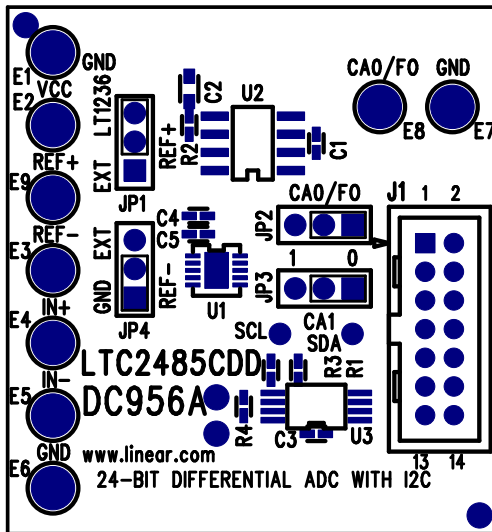


# REVISIONS

REV	DESCRIPTION	APPR	DATE
A	FINAL FABRICATION	MT	01-06-06



TOP SILKSCREEN

LINEAR TECHNOLOGY

DC956A \* LTC2485CDD

24-BIT DIFFERENTIAL ADC WITH I2C

DATE: 01-06-06

## NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. INSTALL SHUNTS ON JP1 (2&3), JP2-JP4 (1&2).
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
5. ASSY PROCESSES SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.

## APPROVALS

	INIT	DATE
DRAWN		
CHECK		
DESIGN	KIM T.	01-06-06
ENGR	MARK T.	01-06-06

SCALE = NONE



**LINEAR  
TECHNOLOGY**

1630 MCCATHY BLVD  
MILPITAS, CA 95035  
PH: (408)432-1900  
LTC CONFIDENTIAL-  
FOR CUSTOMER USE ONLY

TITLE: TOP ASSEMBLY DRAWING

**24-BIT DIFFERENTIAL ADC WITH I2C**

SIZE	DEMO	REV.
A	DC956A * LTC2485CDD	A

DES- 0000

SHT 1 of 1