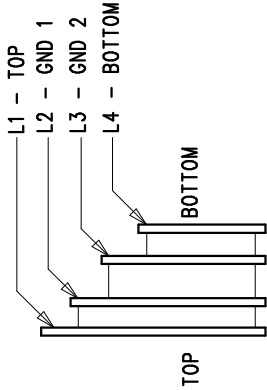
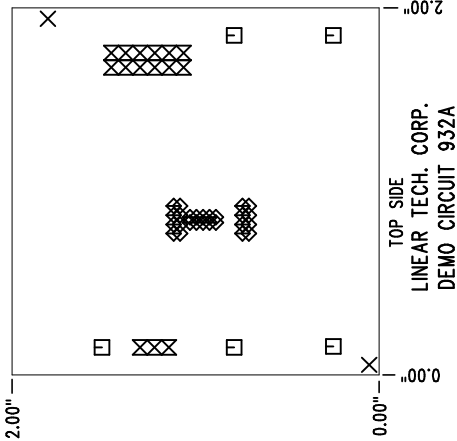
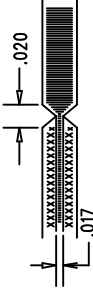


REVISION HISTORY			
ECO	REV	DESCRIPTION	APPR
	2	PRODUCTION	EDWIN L.
			4-9-10



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS
AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
5. FINISH: -SNOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE ROHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
0.07	2	X	NO	+/-0.003
0.095	5	□	YES	+/-0.003
0.01	26	◇	YES	+/-0.003
0.035	15	⊗	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED		APPROVALS	
DIMENSIONS ARE IN INCHES		PCB DES.	HZ
TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		APP ENG.	EDWIN L.
		TITLE: FABRICATION DRAWING	
		LOW VOLTAGE LDO LINEAR REGULATOR	
		SIZE	REV
		N/A	2
		IC NO.	LT3022EDHC
		DEMO CIRCUIT	932A
		SCALE = NONE	
		FILENAME:	DC932A-2.PCB
			SHT 1 OF 1

1830 MCCARTHY BLVD
MILFORD, CT 06455
PH: (408) 532-1800
www.linear.com
LTC CONFIDENTIAL-
FOR CUSTOMER USE ONLY

LINEAR
TECHNOLOGY