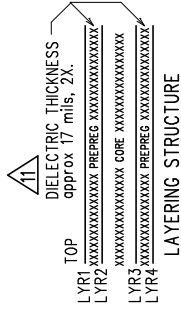
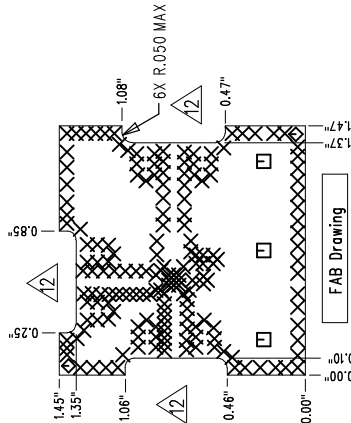
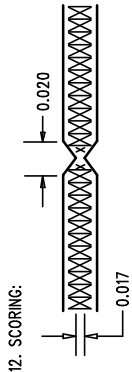



DWG NO		SH	REV
REVISION HISTORY			
ECO	REV	DESCRIPTION	DATE
			APPROVED



- NOTES: UNLESS OTHERWISE SPECIFIED
1. ARTWORK P/N DC910A.
 2. FAB PER IPC-A-600
 3. MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4
FINISHED THICKNESS TO BE .062 +/- .005 INCH
WITH 2 OZ. COPPER ON TWO OUTER LAYERS AND
1 OZ. COPPER ON TWO INTERNAL LAYERS.
FLAMABILITY RATING: 94 V-0 MINIMUM
 4. OUTER DIELECTRIC THICKNESS: TARGET 50-OHM USING 32 MIL
TRACE. SEE STACKUP DIAGRAM.
 5. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
-A- AND -B- ARE PRIMARY DATUMS.
 6. BOARD: SELECTIVE PLATED BOARD. SOLDER
MASK OVER BARE COPPER.
WHITE TIN BOTH SIDES.
SOLDER MASK TYPE: WET PHOTO-IMAGEABLE
THE PCB COLOR: GREEN
SILKSCREEN ONE SIDE WITH WHITE NON-CONDUCTIVE
INK.
PLATE THRU ALL HOLES WITH COPPER
MIN. PLATING THICKNESS: 1 OZ. EXCEPT
WHERE PLATING NOT REQUIRED
7. DRILL: ALL HOLES SHALL BE DRILLED +/- .003 INCH
WITH RESPECT TO CTR. OF DRILLED PAD.
ALL HOLES FINISHED SIZE AFTER PLATING.
8. DROP ALL UNUSED PADS ON INNER LAYERS.
9. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
10. CONTROLLED 50 OHM IMPEDANCE(AT 2.5GHz FREQ.) FOR LAYER 1-2.
11. 17 MILS THICKNESS REFLECTS DIELECTRIC CONSTANT OF 4.1 @ 2GHz.
SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC
CONSTANT DEVIATIONS.
12. INNER AND OUTER LAYER COPPER SHALL BE EXPOSED IN
TWO INSET AREAS ALONG BOARD EDGES.
DO NOT MODIFY INNER LAYER COPPER BACKOFF OUTLINE.

SIZE	QTY	SYM	PLTD
10	175	X	PLTD
93	3	□	PLTD
70	2	◇	NPLTD



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON ANGLE ±1 ° 2 PLACE ±.01 3 PLACE ±.005 INTERPRET DIM AND TOL PER ASME Y14.5M – 1994	CONTRACT NO		LINEAR TECHNOLOGY				1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 Fax: (408)434-0507	
	APPROVALS	DATE	TITLE LT5557EUF DOWNCONVERTING MIXER W/DISCRETE IF BALUN					
	DRAWN JUNE WU	4/4/05						
	CHECKED							
		APPROVED		SIZE A	CAGE CODE	DWG NO	REV A	
THIRD ANGLE PROJECTION		ENGINEER						
DO NOT SCALE DRAWING			SCALE 1/1	FILENAME:		SHEET 1 OF 1		

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