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Customer Notice: Linear Technology has made a best effort to design a circuit that meets customer-supplied specifications; however, it remains the customers responsibility to verify proper and reliable operation in the actual application. Component substitution and printed circuit board layout may significantly affect circuit performance or reliability. Contact Linear Applications Engineering for assistance.

APPROVALS

DRAWN: Rudy Bautista

ENGINEER: David Canny

APPROVED:

CHECKED:

DCxxx_A_00.PCB xxxA_x.DSN

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Title Micropower Synchronous Buck-Boost Converter
for CDMA and HSDPA

Size Document Number

DEMO CIRCUIT 901A

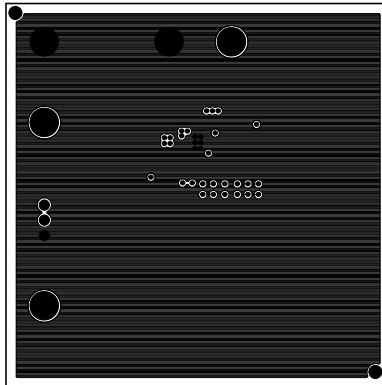
Rev
1

Date: Wednesday, March 01, 2006

Sheet 1 of 1

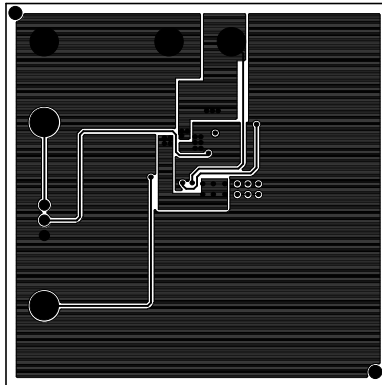
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Demo Circuit 901A

Layer2



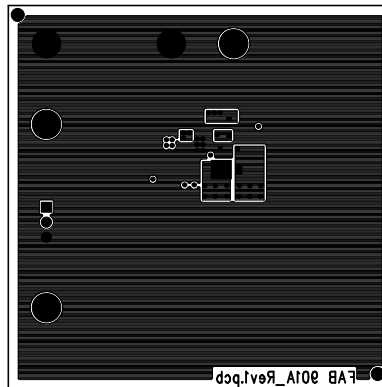
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Layer3



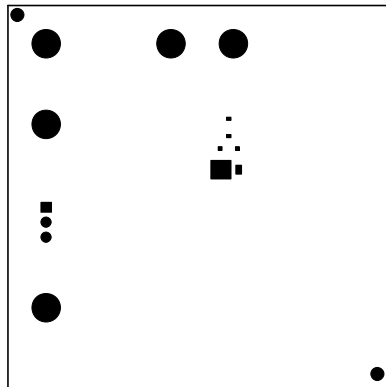
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Solder Side

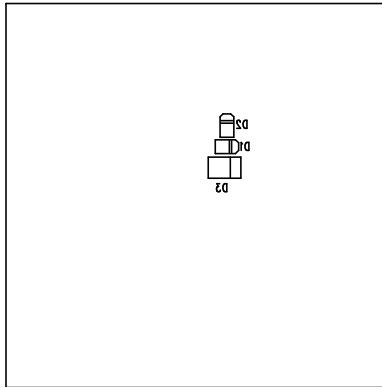


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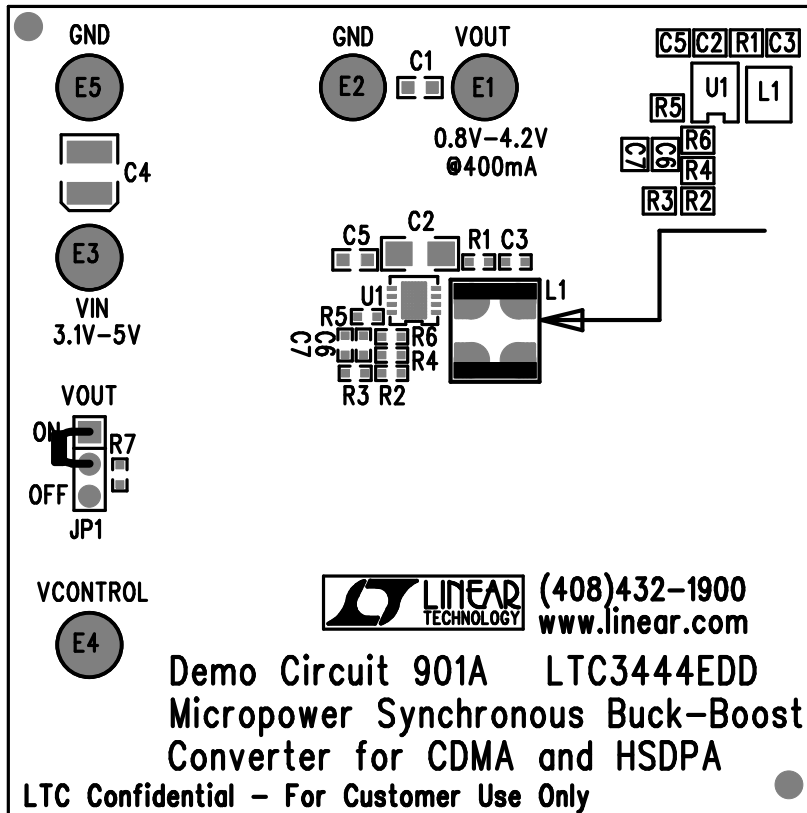
SolderMask Bottom




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Demo Circuit 901A
Silkscreen Bottom



Linear Tech. Corp.
Demo Circuit 901A
Silkscreen Top



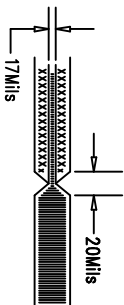
Assembly, Top Drawing

APPROVALS			 LINEAR TECHNOLOGY 1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900		
	INIT	DATE			
DRAWN			TITLE: Micropower Synchronous Buck-Boost Converter for CDMA and HSDPA		
CHECK					
DESIGN	RmB				
ENGR	David C				
			SIZE NONE Demo Circuit 901A REV. 1		
SCALE = NONE			DES-23xxxx		SHT 1 of 1


REVISIONS			
REV		APPR	DATE

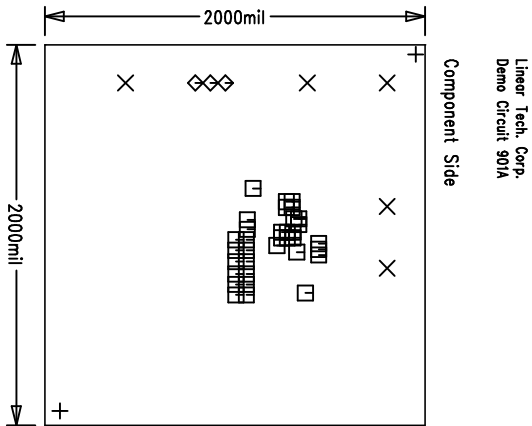
NOTES: UNLESS OTHERWISE SPECIFIED:

- ARTWORK P/N Demo Circuit 901A Rev1
- FAB PER IPC-A-600, 4-Layers.
- MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4 .062 +/- .005 INCH THICKNESS WITH 2 OZ. COPPER FINISH ON TWO OUTER LAYERS AND 1 OZ. COPPER ON TWO INTERNAL LAYERS. FLAMABILITY RATING: 94 V-2 MINIMUM .
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN. 0.00 ARE PRIMARY DATUMS.
- BOARD: SELECTIVE PLATED BOARD. SOLDER MASK OVER BARE COPPER, COLOR, GREEN LPI. WHITE TIN IMMERSION (OMIKRON) BOTH SIDES. SILKSCREEN COMPONENT SIDE WITH WHITE NON-CONDUCTIVE INK. PLATE THRU ALL HOLES WITH COPPER MIN. PLATING THICKNESS: 1 OZ. EXCEPT WHERE PLATING NOT REQUIRED
- DRILL: ALL HOLES SHALL BE DRILLED +/- .003 INCH WITH RESPECT TO CTR. OF DRILLED PAD. ALL HOLES FINISHED SIZE AFTER PLATING.
- DROP ALL UNUSED PADS ON INNER LAYERS.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- SCORING:



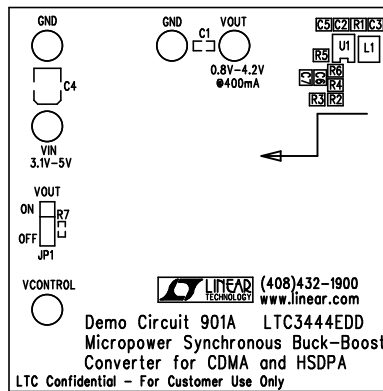
Fabrication Drawing

APPROVALS		 LINEAR TECHNOLOGY 1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900
DRAWN	INT DATE	
CHECK		
DESIGN	Rmb	
ENGR	David C	
TITLE: Micropower Synchronous Buck-Boost Converter for CDMA and HSDPA		
SIZE	NONE	Demo Circuit 901A
		REV. 1
SCALE = NONE		DES-23xxx
		SHT 1 of 1



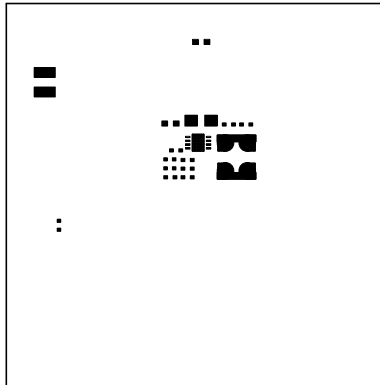
SIZE	QTY	SYM	PLTD	TOL
70	2	+	NO	+/-3Mil
94	5	X	YES	+/-3Mil
10	34	□	YES	+/-3Mil
31	3	◇	YES	+/-3Mil

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Silkscreen Top

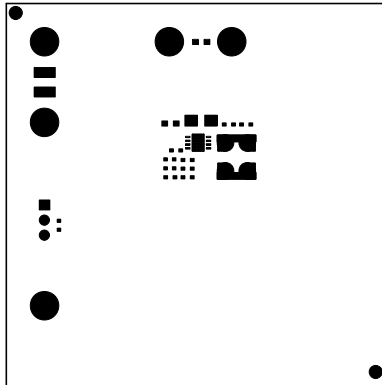


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PasteMask Top



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SolderMask Top



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Component Side

