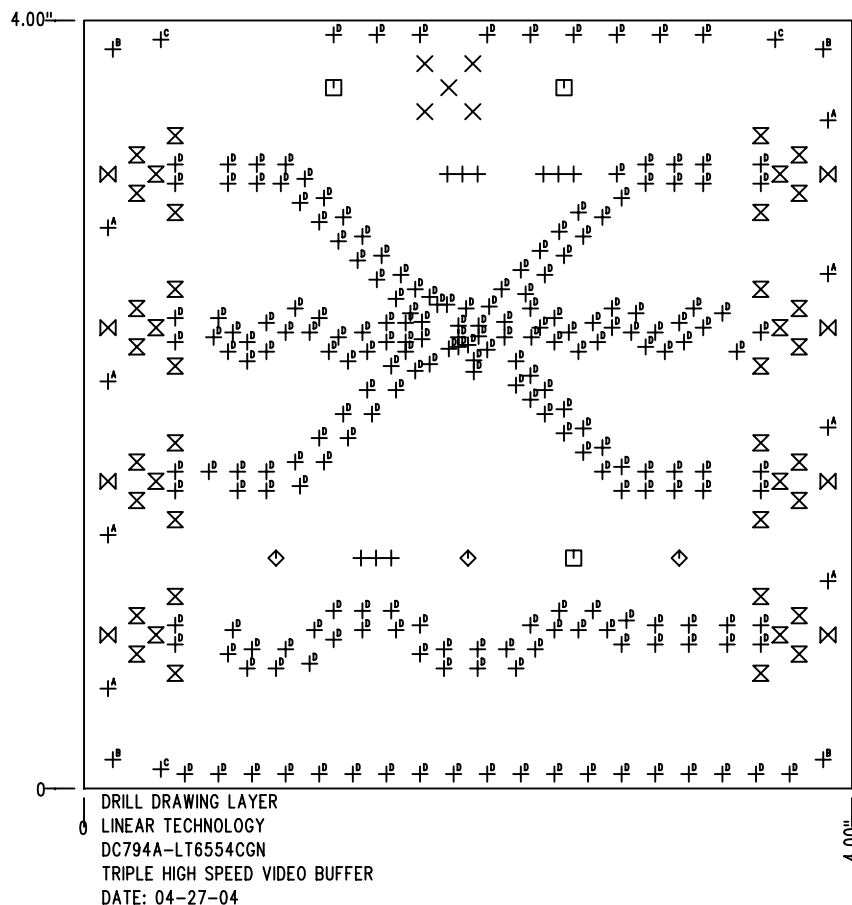
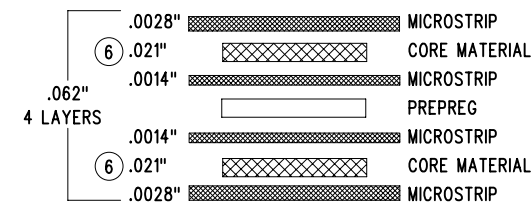


| REVISIONS | | | |
|-----------|-------------------|------|------|
| REV | DESCRIPTION | APPR | DATE |
| A | PROTOTYPE RELEASE | | |



| SIZE | QTY | SYM | PLTD |
|-------|-----|-----|-------|
| 0.035 | 9 | + | PLTD |
| 0.055 | 5 | × | PLTD |
| 0.094 | 3 | □ | PLTD |
| 0.21 | 3 | ◇ | PLTD |
| 0.065 | 40 | ⊗ | PLTD |
| 0.1 | 8 | ⊗ | PLTD |
| 0.15 | 8 | A | PLTD |
| 0.125 | 4 | B | PLTD |
| 0.07 | 3 | C | NPLTD |
| 0.015 | 218 | D | PLTD |

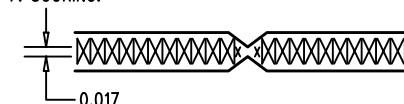


NOTES : Unless Otherwise Specified

1. MATERIAL : FR4 OR EQUIVALENT EPOXY, 2 OZ. COPPER CLAD
THICKNESS 0.062" +/- .006 TOTAL OF 4 LAYERS.
2. FINISH : ALL PLATED HOLES .001 MIN. / .0015 MAX. COPPER PLATE
ELECTRODEPOSITED TIN-LEAD COMPOSITION
SMOBC BEFORE REFLOW.
3. SOLDER MASK : BOTH SIDES USING LPI OR EQUIVALENT.
4. SILKSCREEN : USING WHITE NON-CONDUCTIVE EPOXY INK.
5. CONTROLLED 75 OHM IMPEDANCE (AT 2.5 GHz FREQ.) FOR
LAYER 1-2 AND LAYER 4-3. TRACE WIDTH ARE 0.01" MINIMUM.
AIRGAP SPACING SHOULD BE 0.011".


⑥ SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC
CONSTANT DEVIATIONS. PLEASE CONSULT LTC.

7. SCORING:



8. PLEASE LOOK AT THE README FILE FOR THE OTHER REQUIREMENTS.

SHOWN FROM COMPONENT SIDE

| | | | | | |
|--------------|--------|----------|--|---|------------|
| APPROVALS | | |  LINEAR TECHNOLOGY | 1630 McCarthy Blvd Milpitas, CA 95035 PH: (408)432-1900 | |
| | INIT | DATE | | | |
| DRAWN | | | | | |
| CHECK | | | | | |
| DESIGN | KIM T. | 04-27-04 | | | |
| ENGR | JON M. | 04-27-04 | TITLE: FABRICATION DRAWING | | |
| | | | TRIPLE HIGH SPEED VIDEO BUFFER | | |
| | | | SIZE | DEMO | REV. |
| | | | A | DC794A-LT6554CGN | A |
| SCALE = NONE | | | DES- 0000 | | SHT 1 of 1 |