



SH 1

REV 5

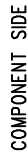
## DESCRIPTION

APPROVED

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REVISED DWG TO MATCH SCHEMATIC AND BOM

3/3/04

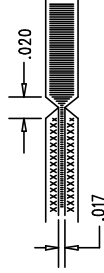


## LAYERING STRUCTURE

HOLE SIZE CHART				
SYM	SIZE	QTY	PTH	
+	.012+/- .003	465	YES	
×	.030+/- .003	14	YES	
□	.064+/- .003	5	YES	
◇	.070+/- .003	2	NO	
⊗	.187+/- .003	4	NO	
⊗				

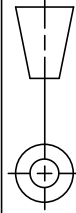
NOTES: UNLESS OTHERWISE SPECIFIED

1. ARTWORK P/N DC699 REV 5.
2. FAB PER IPC-A-600
3. MATERIAL: FR4  
FINISHED THICKNESS TO BE .062 +/- .005 INCH  
WITH 2 OZ. COPPER ON TWO OUTER LAYERS AND  
1 OZ. COPPER ON TWO INTERNAL LAYERS.  
FLAMABILITY RATING: 94 V-0 MINIMUM
4. OUTER DIELECTRIC THICKNESS: TARGET 50-OHM USING 8 MIL  
TRACE CONTROLLED 50 OHM IMPEDANCE LAYER 1 TO  
LAYER 2. (TO BE FINALIZED BY MANUFACTURER)
5. SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON  
DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.
6. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
-A- AND -B- ARE PRIMARY DATUMS.
7. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH  
HOLES WITH COPPER, .001 INCH THICK MIN. ALL  
HOLE SIZES ARE SPECIFIED AFTER PLATING.  
HOLE LOCATION TOLERANCES ARE +/- .003  
INCH IN RELATION TO CENTER
8. FINISH: SMOBC USING LPI BOTH SIDES, GREEN PREFERABLE.  
SILKSREEN BOTH SIDES WITH WHITE NON-CONDUCTIVE  
INK.
9. DROP ALL UNUSED PADS ON INNER LAYERS.
10. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
11. SCORING: (FOR PRODUCTION RELEASE ONLY)



UNLESS OTHERWISE SPECIFIED  
DIMENSIONS ARE IN INCHES  
TOLERANCES ON ANGLE  $\pm$  °  
2 PLACE  $\pm$  .01 3 PLACE  $\pm$  .005  
INTERPRET DIM AND TOL  
PER ASME Y14.5M - 1994

### THIRD ANGLE PROJECTION



DO NOT SCALE DRAWING

CONTRACT NO.

APPROVALS	DATE
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DRAWN	I SANTOS	7/31/00
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**CHECKED**

**ENGINEER**

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TITLE

FAB, LTC1742 14-BIT/65 MSPS DEMO PCB

SIZE A	CAGE CODE	DWG NO	REV 5
		DC699A	

SCALE 1/1  
FILENAME: 699Ar5.PCB  
SHEET 1 OF 1