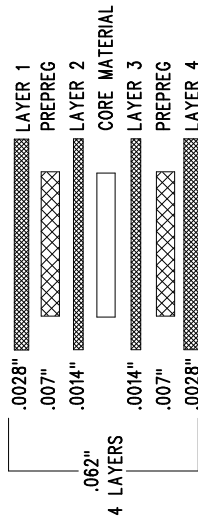


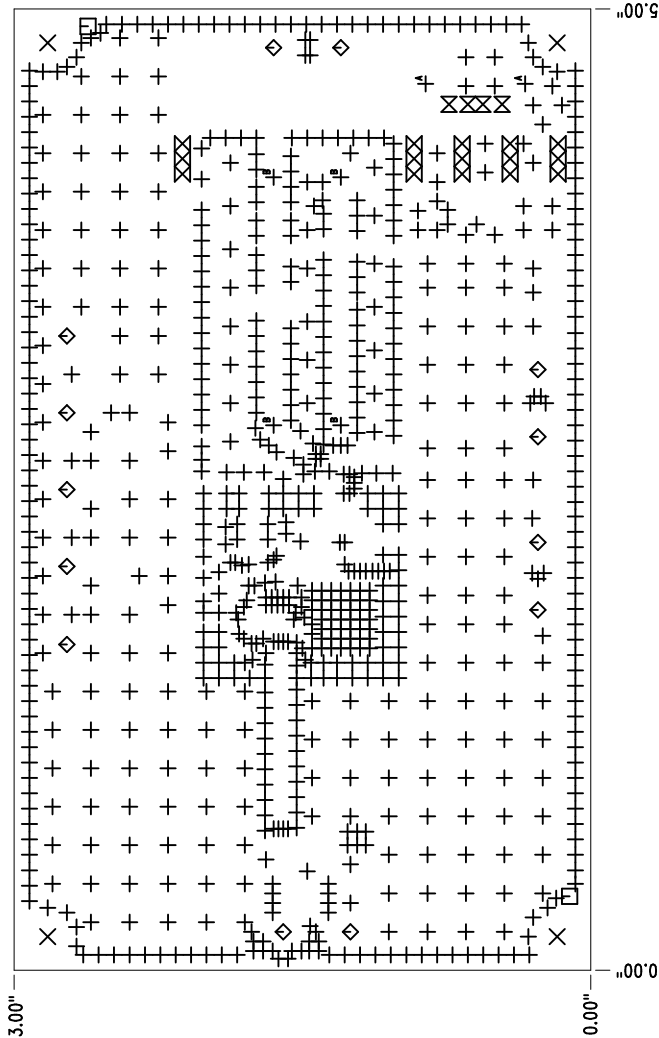
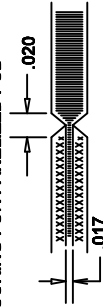
REVISION HISTORY			
ECO/REV	DESCRIPTION	APP. ENG.	DATE
-	4	WALKER B.	5-17-13

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-800.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 ±.005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/0.003 IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREEN: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE ROHS COMPLIANT.
8. SCORING FOR PANELIZED PCB



SIZE	QTY	SYM	PLATED	TOL
0.01	827	+	YES	+/-0.003
0.187	4	X	NO	+/-0.003
0.07	2	□	NO	+/-0.003
0.094	13	◇	YES	+/-0.003
0.035	15	⊗	YES	+/-0.003
0.03937	4	⊗	YES	+/-0.003
0.09055	2	⊕	YES	+/-0.003
0.065	4	⊖	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED

DIMENSIONS ARE IN INCHES

TOLERANCES:

0.XX = ± 0.01

0.XXX = ± 0.005

INTERPRET DIM AND TOL PER ASME Y14.5M-1994

THIRD ANGLE PROJECTION

APPROVALS

PCB DES. JW

APP ENG. WALKER B.

SCALE = 1 : 1

1600 MCCARTHY BLVD
MILPITAS, CA 95035
P.O. BOX 452-1800
LTC CONFIDENTIAL
FOR CUSTOMER USE ONLY

LINEAR TECHNOLOGY

TITLE: FABRICATION DRAWING

USB 5V 2.5A OUTPUT, 42V INPUT

SYNCHRONOUS BUCK WITH CABLE DROP COMPENSATION

SIZE IC NO. LT8697EUDD

REV N/A DEMO CIRCUIT 1973A

4

FILENAME: DC1973A-4.PCB

SHT 1 OF 1