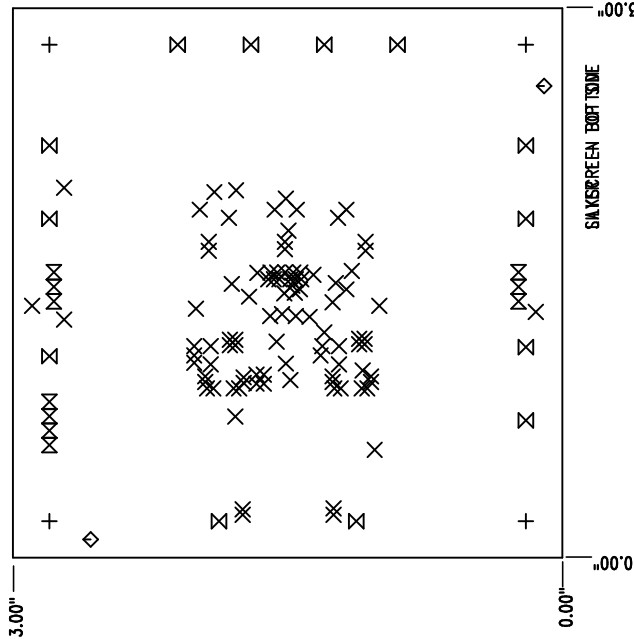
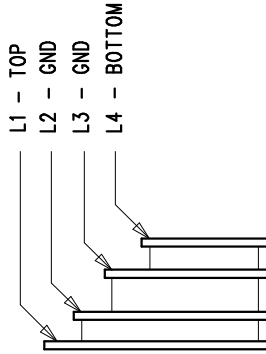


REVISION HISTORY			
ECO	REV	DESCRIPTION	DATE
	1	PRODUCTION	1-24-11

SHOWN FROM TOP SIDE

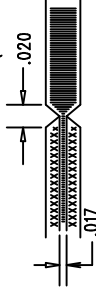


LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062" +/- .005"  
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE: PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK &g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



UNLESS OTHERWISE SPECIFIED		APPROVALS		LINEAR TECHNOLOGY	
DIMENSIONS ARE IN INCHES		PCB DES.	HZ	1520 MCARTHY BLVD MARTIN, CA 94035 PH: (408) 532-1900 WWW.LINEAR.COM LTC CONFIDENTIAL - FOR CUSTOMER USE ONLY	
TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005"		APP ENG.	Y1 S.	TITLE: FABRICATION DRAWING	
INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION				DUAL OUTPUT, DUAL PHASE 5A SYNCHRONOUS BUCK CONVERTER	
				SIZE IC NO. LTC3869IGN-2	
				N/A DEMO CIRCUIT 1798A	
				REV 1	
		SCALE = NONE		FILENAME: DC1798A-1.PCB	
				SHT 1 OF 1	