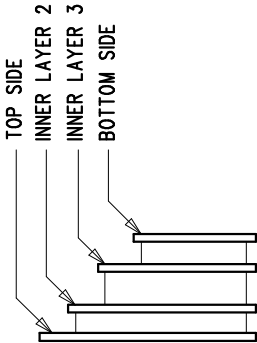


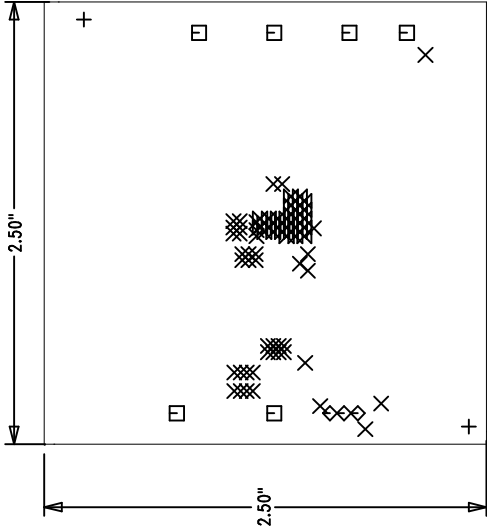
REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG. DATE
-	1	PRODUCTION	JAINO P. 11-15-11

LAYER STRUCTURE



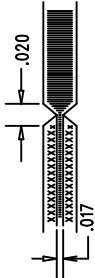
NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4  
-FINISHED THICKNESS TO BE 0.062" +/- .005"  
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS  
AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH  
HOLES WITH COPPER, 0.001" THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003"  
IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)  
-FOR SILKSCREEN: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE ROHS COMPLIANT.
8. DESIGN HAS SOLDER MASK DEFINED\_PADS ON U1. U1 SOLDER MASK  
SIZE IS 0.63MM AND PAD SIZE IS 0.73MM. DO NOT CHANGE ANY  
SIZE ON THIS COMPONENT.



SIZE	QTY	SYM	PLATED	TOL
0.07	2	+	NO	+/-0.003
0.01	42	X	YES	+/-0.003
0.095	6	□	YES	+/-0.003
0.035	3	◇	YES	+/-0.003
0.008	35	⊗	YES	+/-0.003

9. SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	
TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION	

APPROVALS		LINEAR TECHNOLOGY	
PCB DES.	JUNE W.	1530 MCCARTHY BLVD MILFORD, CT 06455 PH: (409)432-1900 WWW.LINEAR.COM LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
APP ENG.	JAINO P.	TITLE: FABRICATION DRAWING 36V, 600mA STEP-DOWN $\mu$ MODULE REGULATOR WITH 5uA QUIESCENT CURRENT	
SCALE = NONE		SIZE	REV
		N/A	1
		IC NO.	LTW8029EY
		DEMO CIRCUIT	1724A
		FILENAME:	1724A-1.PCB
		SHT 1 OF 1	