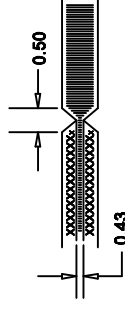


SHOWN FROM TOP SIDE



REVISION HISTORY

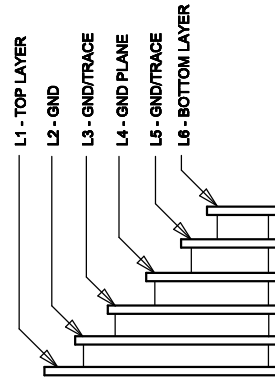
ECO	REV	DESCRIPTION	APPROVAL	DATE
	3	THIRD PROTOTYPE	TOM G.	05/12/11



NOTES: UNLESS OTHERWISE SPECIFIED,

1. FAB PER IPC-A-600
2. MATERIAL:- LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HFR OR EQUIVALENT.
 - FINISHED THICKNESS TO BE 1.6 ± 0.13
 - TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
 - FLAMMABILITY RATING: 94 V-0 MINIMUM .
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.025 THICK MIN.
ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE ± 0.08
IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
 - GOLD IMMERSION BOTH SIDES.
 - SILKSREEN BOTH SIDES WITH WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs SHALL BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):

SIZE	QTY	SYM	PLATED	TOL
0.25	65	□	YES	+/-0.08
0.508	6	⊠	YES	+/-0.08
0.9	26	◇	YES	+/-0.08
1.78	2	+	NO	+/-0.08
2.4	17	×	YES	+/-0.13

LAYER STRUCTURE



<div>UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MILLIMETERS TOLERANCES ON ANGLE ±1° XX±0.25 XXX±0.127 INTERPRET DIM AND TOL PER ASME Y14.5M-1984 THIRD ANGLE PROJECTION</div>	APPROVALS			<div><div>1650 MCCARTHY BLVD WAX, CA 94085 PH: (415) 941-1000 WWW.LINEAR.COM LIT-CO-0001-REV. 10/90 LIT-CO-CONFIDENTIAL- FOR CUSTOMER USE ONLY</div></div>
	PCB DES.	MI		
	APP. ENG.	TOM G.		
TITLE: FABRICATION DRAWING, DUAL DDR MONOLITHIC SYNCHRONOUS STEP-DOWN REGULATOR				
	SIZE	IC NO.	REV	
	N/A	LTC3634	3	
DO NOT SCALE DRAWING	FILENAME:	1708A-3.PCB		SHT 1 of 1
	SCALE: NONE			