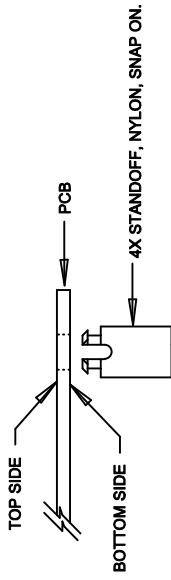


REVISION HISTORY			
ECO	REV	DESCRIPTION	DATE
	1	PRODUCTION	07/12/11

NOTES: UNLESS OTHERWISE SPECIFIED,

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANLIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. INSTALL 4 STANDOFFS AT 4 LOCATIONS AS SHOWN BELOW:



ASSY	IC
- A	U1, U2
- B	U1-U3
- C	U1-U4

MARK APPROPRIATE DASH LETTER

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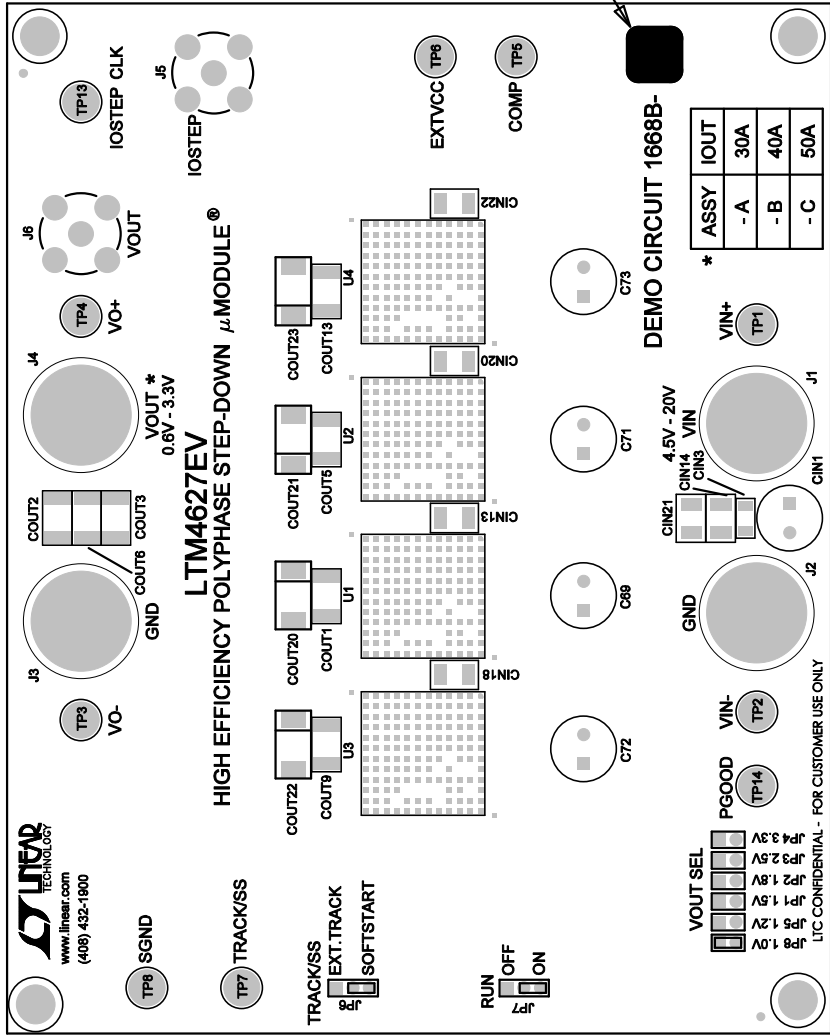
APPROVALS

PCB DES.	MI
APP. ENG.	SAM Y.
SCALE: NONE	

**TITLE: TOP ASSEMBLY DRAWING,
HIGH EFFICIENCY POLYPHASE
STEP-DOWN POWER μ MODULE®**

SIZE	IC NO.	REV
N/A	LTM4627EV	1
	PCB NO.	DEMO CIRCUIT 1668B

FILENAME: 1668B-1.PCB SHT 1 of 2



TOP SILKSCREEN