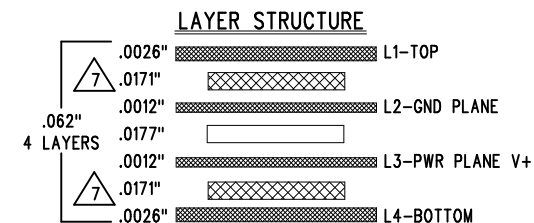
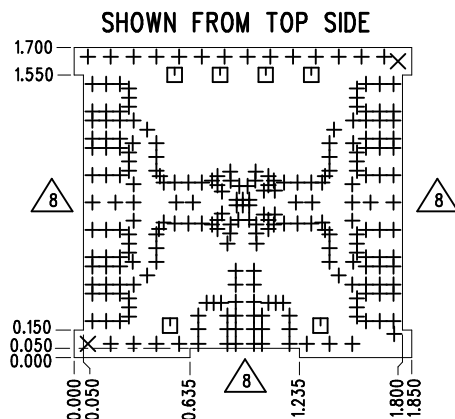
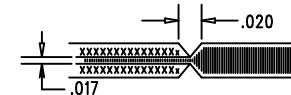


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	JOHN C.	09-12-11



# **NOTES: UNLESS OTHERWISE SPECIFIED**

- FAB PER IPC-A-600.
- MATERIAL: -EPOXY FIBERGLASS, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062" +/- .005"  
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREEN: TOP SIDE USE WHITE NON-CONDUCTIVE INK.
- CONTROLLED 50 OHM +/-5% IMPEDANCE FOR LAYER 1-2  
TRACE WIDTH 0.030 (AT 100MHZ FREQUENCY)
- SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.
- INNER AND OUTER LAYER COPPER SHALL BE EXPOSED ALONG BOARD EDGES. DO NOT MODIFY INNER LAYER COPPER BACKOFF OUTLINE (SMA CONNECTOR).
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
0.01	244	+	YES	+/-0.003
0.07	2	X	NO	+/-0.003
0.065	6	□	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED		APPROVALS			
DIMENSIONS ARE IN INCHES		PCB DES.	LT		
TOLERANCES:		APP ENG.	JOHN C.	TITLE: FABRICATION DRAWING	
0.XX" = ±0.01"				ADC BUFFER	
0.XXX" = ±0.005"					
INTERPRET DIM AND TOL PER ASME Y14.5M-1994				SIZE	IC NO.
THIRD ANGLE PROJECTION				N/A	LTC6417CUDC
					DEMO CIRCUIT 1660B
		SCALE = NONE		FILENAME: DC1660B-2.PCB	REV 2
				SHT 1 OF 1	

1630 MCCARTHY BLVD  
MILPITAS, CA 95035  
PH: (408)432-1900  
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