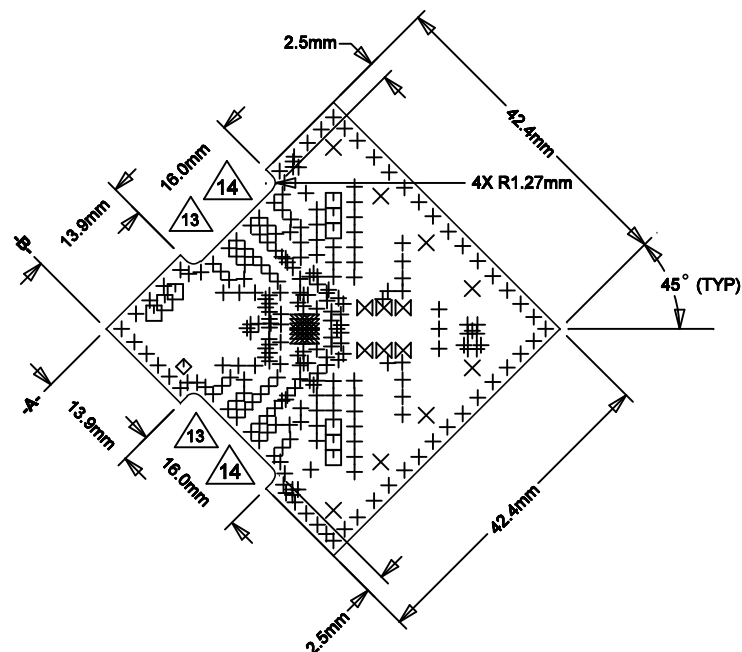
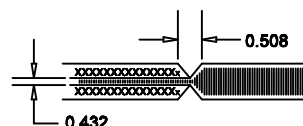


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVAL	DATE
	2	PRODUCTION	ANDY M.	05/20/10

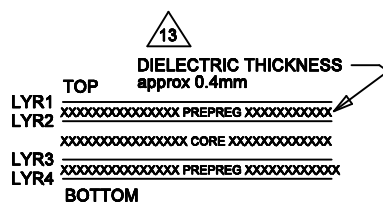


SIZE	QTY	SYM	PLATED	TOL
0.16	16	⊗	YES	+/-0.08
0.2032	249	⊕	YES	+/-0.08
0.508	6	⊗	YES	+/-0.13
0.762	9	⊕	YES	+/-0.13
1.63	1	⊕	YES	+/-0.13
2.36	8	⊗	YES	+/-0.13

15. SCORING:

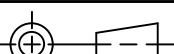



LAYER STRUCTURE



- NOTES: UNLESS OTHERWISE SPECIFIED,**

1. ARTWORK P/N DC1599A REV 2.
2. FAB PER IPC-A-600
3. PCB SHALL BE RoHS COMPLIANT.
4. MATERIAL:
 - A. LAMINATE: EPOXY & PREPREG, NELCO N4000-13.
FLAMMABILITY RATING: 94 V-2 MINIMUM .
 - B. THICKNESS: 1.6 +/- 0.1 mm
 - C. CLAD: 2 OZ. (50um) EXTERNAL LAYERS.
1 OZ. (25um) COPPER ON TWO INTERNAL LAYERS.
5. FINISH:
 - A. COATING: SOLDERMASK OVER BARE COPPER,
COLOR, GREEN LPI.
 - B. PLATING: GOLD IMMERSION BOTH SIDES.
 - C. SILKSCREEN: BOTH SIDES WITH WHITE
NON-CONDUCTIVE INK.
6. TOLERANCES:
 - A. WARPAGE: (0.075mm) MAX. ALONG THE LONGEST DIAG.
AS PER IPC-TM650 METHOD 2.4.22.
 - B. ETCHING: +0.03mm, -0.3mm OF MASTER PATTERN.
 - C. REGISTRATION: 0.075mm MAX.
 - D. MASK THICKNESS: 0.025mm MIN., 0.075mm MAX.
 - E. HOLE PLATING: 22um +/- 3um.
7. SIZE:
 - A. CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
8. HOLE DIAMETERS ARE POST-PLATED SIZES.
9. DEBURR AND BREAK ALL SHARP EDGES.
10. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
11. OUTER DIELECTRIC THICKNESS: TARGET 50-OHM USING
0.762mm TRACE. SEE STACKUP DIAGRAM.
12. CONTROLLED 50 OHM IMPEDANCE (AT 3GHz FREQ.)
FOR LAYERS 1-2. (+/- 5%)
13. 0.4mm THICKNESS REFLECTS DIELECTRIC CONSTANT OF
3.7@ 2GHz SUBJECT TO CHANGE BY MANUFACTURER,
DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS.
14. INNER AND OUTER LAYER COPPER SHALL BE
EXPOSED ALONG BOARD EDGES. DO NOT MODIFY
INNER LAYER COPPER BACKOFF OUTLINE.

<div>UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MILLIMETERS TOLERANCES ON ANGLE ±1° XX ± 0.254 XXX ± 0.127 INTERPRET DIM AND TOL PER ASME Y14.5M-1984</div> <div>THIRD ANGLE PROJECTION</div> <div></div> <div>DO NOT SCALE DRAWING</div>	<div>APPROVALS</div> <table><tr><td>PCB DES.</td><td>MI</td></tr><tr><td>APP. ENG.</td><td>ANDY M.</td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr></table>		PCB DES.	MI	APP. ENG.	ANDY M.					<div></div> <div>1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL FOR CUSTOMER USE ONLY</div>
	PCB DES.	MI									
	APP. ENG.	ANDY M.									
		<div>TITLE: FABRICATION DRAWING, DUAL 6GHz RMS POWER DETECTOR</div>									
		<table><tr><td>SIZE</td><td>IC NO</td><td>LTC5583IUFD DEMO CIRCUIT 1599A</td><td>REV</td></tr><tr><td>N/A</td><td></td><td></td><td>2</td></tr></table>	SIZE	IC NO	LTC5583IUFD DEMO CIRCUIT 1599A	REV	N/A			2	
SIZE	IC NO	LTC5583IUFD DEMO CIRCUIT 1599A	REV								
N/A			2								
		<table><tr><td>FILENAME:</td><td>1599A-2.PCB</td><td>SHT 1 of 1</td></tr></table>	FILENAME:	1599A-2.PCB	SHT 1 of 1						
FILENAME:	1599A-2.PCB	SHT 1 of 1									