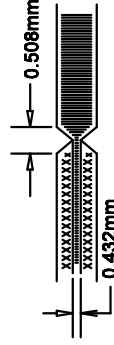


## LAYER CONSTRUCTION

SIZE	QTY	SYM	PLATED	TOL
0.25	55	+	YES	+/-0.08
0.9	21	X	YES	+/-0.08
1.6	5	□	YES	+/-0.13
1.8	2	⊗	NO	+/-0.13



9. DIMENSIONS ARE IN MILLIMETERS  
TOLERANCES ON ANGE  $\pm 1^{\circ}$   
.XX  $\pm 0.25$   
.XXX  $\pm 0.127$

REVISIONS			
REV	DESCRIPTION	APPR	DATE
2	ADDED HEADER TO PIN 3,	LC	08/11/09
	MOVED E2,E4,JP1		

**NOTES: UNLESS OTHERWISE SPECIFIED**

1. FAB PER IPC-A-800
2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4  
-FINISHED THICKNESS SHALL BE 1.6 +/- 0.1mm  
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE  
OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM .
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH  
HOLES WITH COPPER, 0.001" THICK MIN.  
ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
HOLE LOCATION TOLERANCES ARE +/-0.08mm  
IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs SHALL BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:

## APPROVALS

	INITIAL	DATE
DRAWN		
CHECK		
DESIGN	MI	10/09/08
ENGR	LC	08/11/09



**TITLE: TOP ASSEMBLY DRAWING, LTC2463CDD,  
16-BIT DIFFERENTIAL DELTA SIGMA  
I2C ADC**

[illegible]