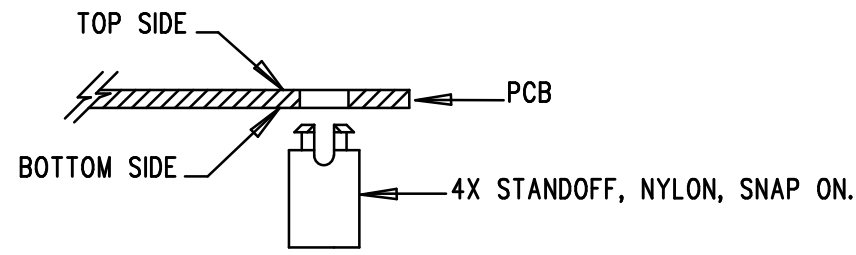



REVISIONS			
REV	DESCRIPTION	APPR	DATE
2	3RD PROTOTYPE	LEO C.	08-05-09

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. INSTALL SHUNTS ON JUMPERS AS SHOWN.
3. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
4. ASSY PROCESSES SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.
5. INSTALL 4 STANDOFFS AT 4 CORNERS AS SHOWN BELOW:



APPROVALS			 LINEAR TECHNOLOGY	1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY					
	INIT	DATE							
DRAWN			TITLE: TOP ASSEMBLY DRAWING 18-BIT PARALLEL INPUT, CURRENT OUTPUT DAC WITH READBACK						
CHECK									
DESIGN	KIM T.	08-05-09							
ENGR	LEO C.	08-05-09							
			<table><tr><td>SIZE</td><td>DEMO</td><td>REV.</td></tr><tr><td>A</td><td>DC1485A-2 * LTC2757CLX</td><td>2</td></tr></table>	SIZE	DEMO	REV.	A	DC1485A-2 * LTC2757CLX	2
SIZE	DEMO	REV.							
A	DC1485A-2 * LTC2757CLX	2							
SCALE = NONE			SHT 1 of 2						