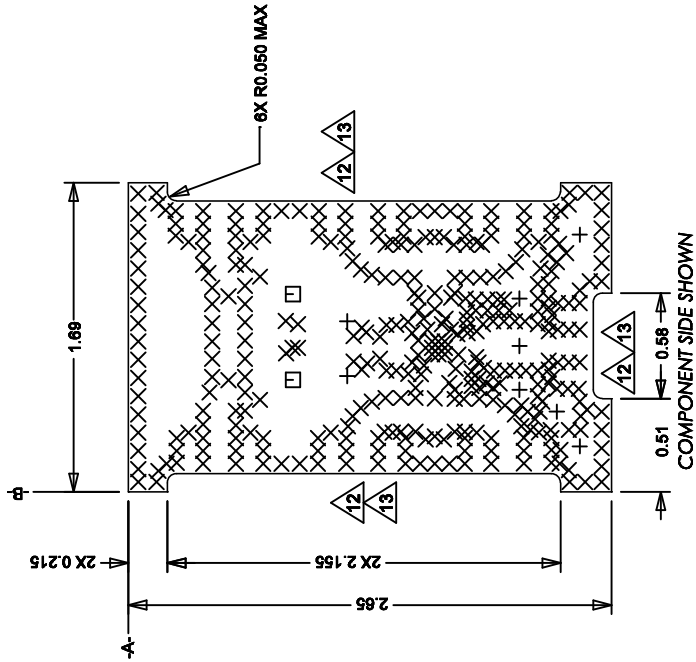
		DWG NO		DC1464A		SH	1	REV	2
REVISION HISTORY									
ECO	REV	DESCRIPTION				DATE		APPROVED	
	2	PROD				04/30/09			

NOTES: UNLESS OTHERWISE SPECIFIED,

- ARTWORK P/N DC1464A REV 2.
- FAB PER IPC-A-600
- PCB SHALL BE RoHS COMPLIANT.
- MATERIAL:
 - LAMINATE: EPOXY FIBERGLASS, NEMA GRADE FR-4
FLAMMABILITY RATING: 94 V-2 MINIMUM.
 - FINISH THICKNESS: 0.062 +/- 0.005
 - CLAD: 1 OZ. (25um) EXTERNAL LAYERS.
1 OZ. (25um) COPPER ON TWO INTERNAL LAYERS.
- FINISH:
 - COATING: SOLDERMASK OVER BARE COPPER,
COLOR, GREEN LPI.
 - PLATING: GOLD IMMERSION BOTH SIDES.
 - SILKSCREEN: COMPONENT SIDE WITH WHITE
NON-CONDUCTIVE INK.
 - FINGER AREA: GOLD PLATE TO MIN. OF 0.0008mm
PER MIL-G-45204, TYP II, CL1, GRADE C, OVER
0.005mm LOW STRESS NICKEL PER QQ-N-290, CL2.
SOLDER MASK TYPE: WET PHOTO-IMAGEABLE
- TOLERANCES:
 - WARPAGE: (0.003) MAX. ALONG THE LONGEST DIAG.
AS PER IPC-TM650 METHOD 2.4.22.
 - ETCHING: +0.0012, -0.012 OF MASTER PATTERN.
 - REGISTRATION: 0.002 MAX.
 - MASK THICKNESS: 0.001 MIN., 0.003 MAX.
 - HOLE PLATING: 22um +/- 3um.
- SIZE:
 - CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
 - HOLE DIAMETERS ARE POST-PLATED SIZES.
 - DEBURR AND BREAK ALL SHARP EDGES.
 - DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
 - OUTER DIELECTRIC THICKNESS: TARGET 50-OHM @ 2GHz
USING 30MIL TRACE. SEE STACKUP DIAGRAM.



13 INNER AND OUTER LAYER COPPER SHALL BE EXPOSED
ALONG BOARD EDGES. DO NOT MODIFY INNER LAYER
COPPER BACKOFF OUTLINE.

12 17 MIL THICKNESS REFLECTS DIELECTRIC CONSTANT OF
4.1 @ 2GHz SUBJECT TO CHANGE BY MANUFACTURER,
DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS.

SIZE	QTY	SYM	PLATED	TOL
10	347	X	YES	+/-0.003
64	8	+	YES	+/-0.005
93	2	□	YES	+/-0.01

11 DIELECTRIC THICKNESS
TOP approx 17 mils
LYR1 X000000000000X PREPREG X0000000000X
LYR2 X000000000000X CORE X0000000000X
LYR3 X000000000000X PREPREG X0000000000X
LYR4
LAYERING STRUCTURE

CONTRACT NO.		LINEAR TECHNOLOGY		1630 McCarthy Blvd. Milpitas, CA 95035		Phone: (408)432-1900 Fax: (408)434-0507	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON ANGLE ±1° 2 PLACE ±0.01 3 PLACE ±.005 INTERPRET DIM AND TOL PER ASME Y14.5M - 1994		APPROVALS		DATE		TITLE	
THIRD ANGLE PROJECTION		DRAWN MEI		08/05/08		FAB, LTC6412CUF, 800MHz ANALOG VGA	
DO NOT SCALE DRAWING		CHECKED				SIZE A	
		APPROVED				CAGE CODE	
		DESIGNED BY				DWG NO. DC1464A	
						REV 2	
						SCALE 1/1	
						FILENAME: 1464A-2.PCB	
						SHEET 1 OF 1	