

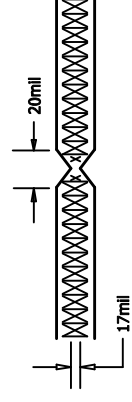
SIZE	QTY	SYM	PLATED	TOL
70	2	+	NO	+/-3mil
94	7	X	YES	+/-3mil
8	11	□	YES	+/-3mil
34	12	◇	YES	+/-3mil
31	3	⊗	YES	+/-3mil

REVISIONS			
REV	DESCRIPTION	APPR	DATE
0	1st Prototype		
2	JP2-JP4 Hole Size (From 26Mil to 34Mil)	RudyB	4/9/09


NOTES : Unless Otherwise Specified

1. FAB PER IPC-A-600.
2. MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4.  
2 OZ. COPPER FINISH ON EXTERNAL LAYERS.  
1 OZ. COPPER FINISH ON INTERNAL LAYERS.  
THICKNESS .062 +/- .006 TOTAL OF 4 LAYERS.  
FLAMMABILITY RATING: 94 V-0 MINIMUM .  
0.00 ARE PRIMARY DATUMS.
3. SIZE: DIMENSIONS SHOWN AND TOLERANCES +/-0.010
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, .001 INCH THICK MIN. ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
HOLE LOCATION TOLERANCES ARE +/-3MIL  
INCH IN RELATION TO CENTER
5. FINISH: SMOBC USING LPI BOTH SIDES COLOR GREEN.  
GOLD IMMERSION BOTH SIDES.
6. SILKSCREEN : USING WHITE NON-CONDUCTIVE EPOXY INK.
7. DO NOT ALTER ARTWORK TO ADD LOGO OR DATE CODE BUT MAY MODIFY PAD SIZE TO MEET END FINISH.
8. PCB's SHOULD BE RoHS COMPLIANT.

9. SCORING:



FABRICATION DRAWING

<b>APPROVALS</b>		 <b>LINEAR</b> TECHNOLOGY		1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900	
DRAWN	INIT	DATE			
CHECK					
DESIGN	RudyB				
ENGR	ThomChen				
			TITLE: LTC3620EDC High Efficiency Monolithic Buck Regulator		
			FD	Demo Circuit 1460A	REV. 2
SCALE = NONE			Sheet 1 of 1		